

# PANNELLO OPERATORE TOUCH SCREEN

TD240

Manuale operatore

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# 1 Introduction

Thank you for choosing a Pixsys instrument.

TD240 is a graphical Touch screen HMI, available also with an integrated PLC. The graphical resources are easily manageable by TdDesigner, a simple and versatile development environment, while the PLC logic is managed by PLProg development environment, which is common to other Pixsys devices.

The waterproof protection of the facade is IP54 and IP30 for the enclosure.

# 2 Model identification

There are two versions of the product. **TD240-AD** is only graphical Touch screen terminal while **TD240-11AD** is the version with integrated PLC expansion.

| Ordering code   |   |    |                               |  |
|-----------------|---|----|-------------------------------|--|
| TD240-          |   |    |                               |  |
| Graphical       | - |    | no PLC expansion integrated   |  |
| touch screen 11 |   |    | with integrated PLC expansion |  |
| terminal        |   |    |                               |  |
| Power           |   | AD | 1224V AC/DC ±15% 50/60Hz      |  |

# 3 Mechanical dimensions and installation



# 4 Display characteristics



| 1 | DISPLAY | <b>Type</b> : Back-lit LCD, resistive touch screen TFT |  |
|---|---------|--|--|
|   |         | Dimensions: Active Area 3.5"                           |  |
|   |         | 70.03(W)mm x 52.56(H)mm                                |  |
|   |         | Resolution: 320x240 pixels                             |  |
|   |         | Colours: 256 (8bit)                                    |  |
|   |         | Importable Images: bitmap of 256 colors(.bmp)          |  |

# 5 Electrical connections



Although this instrument is designed to resist the most difficult conditions in industrial environments, it is good practice to observe the following precautions: • Distinguish supply line from power line

• Avoid proximity with remote control switches, electromagnetic contactors, and powerful motors.

• Avoid placing near power installations, particularly if phase-controlled

# Wiring plan TD240-11AD.



#### 5.1 Terminal block M1





#### 5.2 Terminal block M2

This terminal block is only for TD240-11AD.



The expansion board is provided with 16 pins for digital I/O. Each pin can be used as input or output. If used as output, the tension value generated by the output is also read as input.

| Digital inputs /outputs                         |   |  |  |  |
|---|---|--|--|--|
| I/Q.1÷16  | <ul> <li>Inputs PNP (0-24VDC)</li> </ul>        |  |  |  |
|   | <ul> <li>Static output: 24Vdc – 0,7A</li> </ul> |  |  |  |
| <ul> <li>Max power consumption 4.0 A</li> </ul> |   |  |  |  |

#### 5.3 Terminal block M3

This terminal block is only for TD240-11AD.



|   | Analog outputs  |  |  |  |  |
|---|---|--|--|--|--|
| 1   | CAN- R  | CAN interface, currently not available. Do not |  |  |  |
|   |   | use these pins.                                |  |  |  |
| 2   | CAN- H  | CAN interface, currently not available. Do not |  |  |  |
|   | CAN- H  | use these pins.                                |  |  |  |
| 3   | CAN- L CAN interface, currently not available. Do no      |  |  |  |  |
|   | CAN-L   | use these pins.                                |  |  |  |
| 4   | <b>4 AQ.1</b> Positive for analog output AQ1 (0+12,5 VDC) |  |  |  |  |
| <b>5 AQ.2</b> Positive for analog output AQ2 (0+12,5 VDC) |   | Positive for analog output AQ2 (0÷12,5 VDC)    |  |  |  |
| 6   | 6 AQ.3 Positive for analog output AQ3 (0+12,5 VDC)        |  |  |  |  |
| 7   | AQ.4  | Positive for analog output AQ4 (0÷12,5 VDC)    |  |  |  |
| 8   | 8 AQ.COM Negative common signal for analog outputs        |  |  |  |  |
| 9   | +   | Supply for analog and static outputs           |  |  |  |
| 10  | -   | (Connect 12÷24 VDC)                            |  |  |  |

# 5.4 Terminal block M4

This terminal block is only for TD240-11AD.



| Dig | Digital Inputs/ Outputs               |   |  |  |  |
|-----|---------------------------------------|---|--|--|--|
| 1   | AI-COM                                | Negative common signal for analog inputs. |  |  |  |
| 2   | AI.1 Positive for analog input Al1.   |   |  |  |  |
| 3   | AI.2 Positive for analog input AI2.   |   |  |  |  |
| 4   | 4 AI.3 Positive for analog input Al3. |   |  |  |  |
| 5   | AI.4 Positive for analog input AI4.   |   |  |  |  |

#### 5.5 Serial ports of communication

TD240 terminal communication with other devices is possible through serial connection with **RS485** or **RS232.** 

The electrical signals are available in two connectors present at the back of the terminal: **pin DB9** and **pin DB25**.



| CONNECTOR | PIN N° | SIGNAL            | PORT        |
|-----------|--------|-------------------|-------------|
|           | 1      | Not used          | -           |
|           | 2      | RX – RS232        | COM2        |
|           | 3      | TX – RS232        | COM2        |
|           | 4      | RS485 -           | EXP1        |
| DB9       | 5      | GND RS485 / RS232 | COM2 / EXP1 |
| PINS      | 6      | TX – RS232        | EXP1        |
|           | 7      | RX – RS232        | EXP1        |
|           | 8      | Not used          | -           |
|           | 9      | RS485 +           | EXP1        |



| CONNECTOR    | PIN N° | SIGNAL            | PORT        |
|--------------|--------|-------------------|-------------|
|              | 1      | RX – TTL          | COM2        |
|              | 2      | TX - TTL          | COM2        |
|              | 3      | Not used          | -           |
|              | 4      | Not used          | -           |
|              | 5      | GND RS485 / RS232 | COM2 / EXP1 |
|              | 6      | Not used          | -           |
|              | 7      | GND RS485 / RS232 | COM2 / EXP1 |
|              | 8      | Not used          | -           |
|              | 9      | Not used          | -           |
|              | 10     | Not used          | -           |
|              | 11     | RX – RS232        | COM2        |
| DDDE         | 12     | TX – RS232        | COM2        |
| DB25<br>PINS | 13     | Not used          | -           |
| FINS         | 14     | GND RS485 / RS232 | COM2 / EXP1 |
|              | 15     | RS485+            | EXP1        |
|              | 16     | RS485-            | EXP1        |
|              | 17     | Not used          | -           |
|              | 18     | Not used          | -           |
|              | 19     | Not used          | -           |
|              | 20     | Not used          | -           |
|              | 21     | RX – RS232        | EXP1        |
|              | 22     | TX – RS232        | EXP1        |
|              | 23     | Not used          | -           |
|              | 24     | RS485+            | COM2        |
|              | 25     | RS485-            | COM2        |

## 5.5.1 EXP1 on DB25 connector

The communication port **EXP1** is available in the 25 connector pins in RS232 interface or RS485 (protocol, baud rate, and format are selectable).

#### 5.5.1.1 Interface RS232



| Interfaccia | RS232 su D | B25 (EXP1) |
|-------------|------------|------------|
| 0           | PIN 14     | GND        |
| 0           | PIN 21     | RX-232     |
| 0           | PIN 22     | TX-232     |

# 5.5.1.2 Interface RS485



| Interfaccia | RS485 su | DB25 (EXP1) |
|-------------|----------|-------------|
| 0           | PIN 14   | GND         |
| 0           | PIN 15   | RS485+      |
| 0           | PIN 16   | RS485-      |

#### 5.5.1.2.1 Cable of EXP1 communications in generic RS485

A cable is available (**cod. art. 1620.00.057**, optional), which provides **EXP1** port from connector DB25 in RS485 for a generic connection with other devices (for details regarding the communication protocols, consult other documentation).



## 5.5.1.2.2 Cable of EXP1 comm. in RS485 for PL250 / PL300

For communication with other Pixsys devices (**PL250-XXAD** and **PL300-XXAD**) an (optional) cable is available that connects port **EXP1** in RS485 from connector DB25 of the terminal to port COM1 on PLUG of the PLC.



#### 5.5.2 EXP1 on DB9 connector pins

The communication port **EXP1** is available in the DB9 connector pins in RS232 interface or RS485 (protocol, baud rate, and format are selectable).



| Interfaccia | RS232 su | DB09 (EXP1) |
|-------------|----------|-------------|
| 0           | PIN 5    | GND         |
| 0           | PIN 6    | TX - RS232  |
| 0           | PIN 7    | RX - RS232  |

#### 5.5.2.2 Interface RS485



| Interfaccia | <b>RS485</b> | su DB09 (EXP1) |
|-------------|--------------|----------------|
| 0           | PIN 5        | GND            |
| 0           | PIN 9        | RS485+         |
| 0           | PIN 4        | RS485-         |

5.5.2.2.1 Cable of EXP1 comm. in generic RS485

A cable is available (**code art. 1620.00.034**, optional), which provides EXP1 port from connector DB9 in RS485 for a generic connection with other devices (for details regarding the communication protocols, consult other documentation).



#### 5.5.2.2.2 Cable of EXP1 comm. in RS485 for PL250/PL260/ PL300

For communication with other Pixsys devices (**PL250-XXAD** and **PL300-XXAD**) an (optional) cable is available that connects port **EXP1** in RS485 from connector DB9 of the terminal to port COM1 on PLUG of the PLC.



# 5.5.3 COM2 on DB9 connector

The communication port **COM2** is available in the 9-pin connector, interface RS232 (protocol **MODBUS SLAVE**, format **8,N,1**, baud rate selectable).

Usually this is the communications port used for programming the terminal through a PC (see Chapter 4).

# 5.5.3.1 Interface RS232



| Interfaccia | RS232 su | DB09 (COM2) |
|-------------|----------|-------------|
| 0           | PIN 5    | GND         |
| 0           | PIN 3    | TX - RS232  |
| 0           | PIN 2    | RX - RS232  |

## 5.5.4 COM2 on DB25 connector pins

The communication port **COM2** is available in the 25-pin connector, in interface RS232 or RS485 (protocol **MODBUS SLAVE**, format **8,N,1**, baud rate settable).

Usually this is the communications port used for programming the terminal through a PC (see Chapter 5).

# 5.5.4.1 Interface RS232



| Interfaccia | RS232 su | DB25 (COM2) |
|-------------|----------|-------------|
| 0           | PIN 7    | GND         |
| 0           | PIN 12   | TX - RS232  |
| 0           | PIN 11   | RX - RS232  |

#### 5.5.4.2 Interface RS485



| Interfaccia | RS485 su | DB25 (COM2) |
|-------------|----------|-------------|
| 0           | PIN 14   | GND         |
| 0           | PIN 24   | RS485+      |
| 0           | PIN 25   | RS485-      |

# 6 Setting of dip-switches for analog inputs AI

TD240-11AD is provided with some internal dip-switches (which may be accessed also externally through a hole on the enclosure) for the setting of analog inputs.

For most configurations the settings of inputs AI1..AI4 are similar. Each input is configurable by a dip-switch composed of 4 selectors (except for AI1 which has a 6pins dip-switch, but the first 4 selectors must be configured as for all other analog inputs). Correspondance between analog inputs and their relevant dipswitch is shown in the diagram below:



# N.B.: The device must be switched-off before proceeding to any hardware configuration!

#### 6.1 Setting of dip-switches for analog inputs Al1..4

To obtain the required type of input select the relevant dip-switch according to the table below:

| Type of input | <b>Dip-switch</b> | Notes   |  |
|---------------|-------------------|---|--|
| Desabled      | 0N<br>1 2 3 4     | If analog input is not used, keep<br>the dip-switches open as in the<br>picture |  |
|               |                   | Connect positive signal to analog input and reference signal to pin AI.COM.     |  |
| 010V 16 bit   | 0N<br>1 2 3 4     | Connect positive signal to analog input and reference signal to pin AI-COM.     |  |

| Type of input                                     | <b>Dip-switch</b>   | Notes   |  |
|---|---|---|--|
| 01V<br>020 mV                                     | 0N<br>1 2 3 4   | Connect positive signal to analog input and reference signal to pin AI-COM.   |  |
| 020 mA  |   | Connect positive signal to analog<br>input and any reference pin to the   |  |
| 420 mA  | 1234  | ground of power supply for digital outputs  |  |
| TC<br>K, S, T,<br>R, J, E                         | Connect positive signal of<br>thermocouple to the analog input<br>and the negative signal of<br>thermocouple to pin AI-COM. |   |  |
| PT100<br>NI100                                    | ON<br>1 2 3 4   | For two-wires PT100/NI100 this<br>setting can be selected for all<br>inputs. Connect one wire to<br>analog input and the one to<br>reference pin of AI-COM inputs<br>For 3-wires PT100/NI100 this<br>setting is selectable only for<br>AI1 and AI4. Connect white wire<br>to analog input AI1 or AI4 and the<br>two red wires to the reference pin<br>of AI-COM e to the compensation<br>input of AI2 or AI3. |  |
| Compensa-<br>zione per<br>PT100/NI100<br>a 3 fili | ON<br>1 2 3 4   | For 3-wires PT100/NI100 this<br>setting is selectable only for Al2<br>and Al3, respectively as<br>compensation for inputs Al1 and<br>Al4.   |  |

| Type of input                        | Dip-switch    | Notes   |
|--------------------------------------|---------------|---|
| NTC-10K<br>PT1000<br>PT500<br>PTC-1K | 0N<br>1 2 3 4 | Connect one wire to analog input<br>and the second wire to reference<br>pin of inputs AI-COM.                             |
| LUX Fi<br>LUX Rs                     | 0N<br>1 2 3 4 | Connect one wire of the brightness<br>sensor to analog input<br>and the second wire to reference<br>pin of inputs AI-COM. |

## 6.2 Setting of dip-switches for analog inputs AI5..6

Analog inputs AI5..AI6 are normally selected by SMW127 and SMW128 as "desabled" (this is the setting at reset). But they might be set as 0..10V-10 bit only if inputs AI1 and AI2 are not already selected as 0..10V-10bit. In fact input AI5 is exploiting some hardware resources of AI1 as well as input AI6 respectively to AI2. Selecting AI5 and AI6 as 0..10V-10 bit by means of relevant dipswitches

(see diagrams below), the signal applied to input I5 is subject to analog conversion and the read value is scaled and assigned to AI5, while the signal applied to input I6 is subject to analog conversion and the read value is scaled and assigned to AI6. This way it is possible to get two inputs 0-10V in addition to the four universal analog inputs.

Below the diagrams with the setting of dip-switches for the configuration of analog input AI5.



Below the diagrams with the setting of dip-switches for the configuration of analog input AI6.



# 7 Programming the terminal

In order to program the terminal it is necessary to connect it to a PC. The development kit (optional, **code art. 2100.10.008**) provides the cable and the development environment to create applications.

Programming involves the communication port **COM2**, present in both connectors. There are 2 adapters on the side of the terminal that allow the user to program the terminal through connector DB25 or through DB9.

#### 7.1 Starter Kit – Connection of the terminal to the PC



## 7.2 The development environment

#### The TD240 is a HMI graphical terminal with an integrated PLC.

It allows a centralization of all the operational logic of the system that must be supervised and controlled.

The graphical part of the development environment must manage the visible pages and their fundamental items (e.g. synthesis, push-buttons, numerical and text edit boxes, images) and the interaction between various objects and the memory areas (the memory areas which they must reference for push-buttons, indicators and images).

The logic of the operation of the system, i.e. the way in which the memory areas must interact among each other, is instead managed by the PLC.

**The TD240 terminal is also a PLC**, therefore it manages graphics and logic, leaving other connected PLCs the sole task of "detecting the information" (e.g. digital and analog inputs, encoders etc.) and "to control the actuators" (e.g. digital and analog outputs etc.).

The development environment has two sub-environments:

• **TdDesigner:** manages all resources that are strictly related to the graphics.

• **PLProg:** manages the interactions between the memory areas of the terminal (Ladder code, common to other Pixsys PLCs, essentially the PL250 and TCT500).



Any application managed by the TD240 terminal should therefore be realized using both the development environments, **implementing therefore two different files** strictly connected between them.

The operation of the terminal anticipates a division of the time dedicated to graphics management (implemented with TdDesigner) and of the time dedicated to the management of the PLC (implemented with PLProg 4.xx).

The default setup foresees an equal division of the execution cycle: the terminal will execute the instructions inherent for graphics for 50% of the time, and the Ladder instructions of the PLC for the other 50% (cyclically).



The time division is settable by the user (see chapter 5). An example is shown below in which 80% of the time is dedicated to the graphics and 20% to the PLC.



# 7.2.1 Creation of a new project



To create a new project and transfer it to the terminal, follow the procedure and described below:

1. **Start TdDesigner:** Start the **TdDesigner** software from the Start\Program menu or from the Desktop icon (automatically created at installation).



2. **Create new project** *name\_file.tdproj***:** Once the development environment is opened, create a new project as shown in the figure below:



Select terminal TD240 (320x240 pixel display 3,5")

| Nuovo progetto   |   |   |                |
|--|---|---|----------------|
| Tipo progetto:   |   |   |                |
|  |   |   |                |
| TD 320<br>(320x240)  | TD240<br>(320x240)                      |   |                |
| Nome:  | TD240Project                            |   |                |
| Home.  |   |   |                |
| Percorso:  | C:\Programmi\Pixsys\TdDesigner\Projects |   |                |
|  |   | S | foglia         |
|  |   |   | Crea directory |
| II progetto sarà creato in C:\Programmi\Pixsys\TdDesigner\Projects\TD240Project\TD240Project |   |   |                |
|  | OK Annulla                              |   | ?              |

The development environment can put new project in a directory automatically created with the same name chosen for the projet (*nome\_file*.tdproj), or in a folder chosen by user.

Graphics management is handled in other documentation, available with the development kit (**code art. 2100.10.008**) and assumed here as known by the user.

3. **Start PLProg 4.xx:** Start the **PLProg 4.xx** software from the Start\Program menu or the Desktop icon (automatically created at installation).



4. **Create new Ladder diagram file\_name.plp**: Once the development environment is opened, create a new diagram as shown in the figure below:



A window will now open in the center of the screen: select the terminal TD240 in the item list *Select CPU*.

The guide to the software and the implementation of the ladder code is available with the development kit (**code art. 2100.10.008**) and assumed here as known by the user.

5. **Compile project** *file\_name.tdproj*: Once the implementation of the graphics is finished, it is necessary to **compile** the project, as shown in the figure below.



This operation is necessary to make the project available as soon as implemented to the development environment **PLProg 4.xx**. <u>The compilation has effect only if PLProg is open and the terminal</u> <u>TD240 has been selected as CPU.</u>

6. <u>Compile Ladder diagram file name.plp</u>: Once the Ladder diagram sketch is finished, it is necessary to **compile** it, as shown in the figure below. It is this fundamental passage that creates the link between the Ladder file just compiled in the development environment of **PLProg4.xx** with the file previously compiled in the development environment of **TdDesigner**.

Only with this operation will it be in fact possible to communicate to the terminal also the instructions inherent to the graphics of the created project.



At this point, if saved by PLProg, file file\_name.plp will contain both the PLC part and the graphical part (is not necessary that the file .tdproj has the same name of the file .plp).

7. <u>Transfer the project to the terminal:</u> If the compilation was successful, now one can carry out the download of the project, as shown in the figure below. The procedure transfers both the graphical part and the PLC part to the terminal



If the TD240 is connected correctly to the PC (see diagram of **section 4.1**), during the transfer the terminal will show this figure on the display:



At the end of the download, the terminal will execute the instructions of the entire application.

#### 7.2.2 Modification of an already existing project

In the case in which an already existing project must be modified, follow the procedure below:



For eventual modifications of only the PLC part (as outlined) it is not necessary to start TdDesigner. The compilation of the project file\_name.plp will maintain the graphics unchanged and will activate the modifications of the Ladder diagram.

# 8 Memory areas of the TD240

The TD240 makes memory areas available where it is possible to read or to write program data. Access to the various areas is made possible by instructions that access a single bit (b), a byte (B), a word (W) or a double word (D).

| SIGN | AREA                           | ACCESS  |
|------|--------------------------------|---------|
| V    | Area of Variable V             | b, W, D |
| SM   | Area of Special Marker         | b, W, D |
| l    | Area of Digital Inputs         | b, W    |
| AI   | Area of Analog Inputs          | b, W    |
| Q    | Area of Digital Outputs        | b, W    |
| М    | Area of Marker                 | b, W    |
| В    | Area of Bistable               | b       |
| AQ   | Area of Analog Outputs         | b, W    |
| Т    | Area of Timer                  | b, W    |
| PT   | Area of Preset Timer           | b, W    |
| С    | Area of Counters               | b, W    |
| PV   | Area of Preset Counters        | b, W    |
| EEP  | Area of EEPROM                 | W       |
| MMC  | Area of EEPROM data            | W       |
| EXP  | Area of buffer TX/RX port EXP1 | В       |

#### 8.1 Area of Variable V

Area variable V is a memory area used by the program to retain the data of the operations. It consists of 10000 locations of type word (5000 double word). Access can occur through operations on bits, words or double words. In the last case, the number of double words always makes reference to the organization by words, therefore in order to access consecutive double word variables it is necessary to increment by 2.
The memorized values are maintained even in the absence of power thanks to the rechargeable battery pad. Once charged, the battery maintains memorized data for approximately 6 months.



### 8.2 Area of Special Marker SM

Area special marker SM is the memory area used to retain all the data necessary for the Ladder program to interact with the TD240 hardware.

Some data are initialized at the start with default values indicated in the table below. In this area are the storage words that manage the events relative to the graphics, the PLC control bits and the setup for the serial ports of communication.

The table below describes the content of each single location of the special marker area, indicating the address for access through the ModBus protocol and the operation allowed at this location (R = read, W = write, R/W = read/write). The bits and words that do not appear in the tables are not used.

| SM N° | ModBus<br>Address |          | Description / Meaning  |     |  |  |  |  |
|-------|-------------------|----------|--|-----|--|--|--|--|
| SM0   | 1000              | Bit stat | Bit state  |     |  |  |  |  |
|       |                   | Bit 0    | Bit RUN/STOP (1 = RUN). At startup<br>this bit is always forced ON (PLC in<br>RUN). In STOP the output relays of<br>the PLC are disabled.  | R/W |  |  |  |  |
|       |                   | Bit 1    | Bit always ON for the first scan cycle<br>of the main program. It becomes<br>used, for example, to execute a<br>subprogram of initialization.  | R   |  |  |  |  |
|       |                   | Bit 2    | Bit that allows use of a 60-second<br>clock impulse (ON for 30 seconds,<br>OFF for 30 seconds).  | R   |  |  |  |  |
| Bi    |                   | Bit 3    | Bit that allows use of a 1-second clock impulse (ON for 0.5 seconds, OFF for 0.5 seconds).   | R   |  |  |  |  |
| Bit 4 |                   | Bit 4    | Bit clock of scan cycles that is active<br>(ON) for a cycle and deactivated<br>(OFF) for the successive cycle. It can<br>be used as an input for counting<br>scan cycles.                  | R   |  |  |  |  |
| Bit 7 |                   |          | Bit ON during the transmission<br>phase of data on serial port EXP1. It<br>is automatically switched OFF at the<br>end of the transmission.  | R   |  |  |  |  |
| Bit 8 |                   | Bit 8    | Bit ON during the transmission<br>phase of data on serial port COM2. It<br>is automatically switched OFF at the<br>end of the transmission   | R   |  |  |  |  |
|       |                   | Bit 10   | This bit, if set ON, enables the serial<br>port EXP1 in "modem" mode. That<br>means that the timeout between one<br>character and another in reception is<br>automatically fixed to 40 mS. | R/W |  |  |  |  |

|     |      | Bit 11 | This bit, if set ON, enables the serial                         | R/W         |
|-----|------|--------|---|-------------|
|     |      |        | port COM2 in "modem" mode. That                                 |             |
|     |      |        | means that the timeout between one                              |             |
|     |      |        | character and another in reception is                           |             |
|     |      |        | automatically fixed to 40 mS.                                   |             |
|     |      | Bit 15 | Bit ON to indicate a TD240-11AD;                                | R           |
|     |      |        | bit set to zero to indicate a TD240-                            |             |
|     |      |        | AD  |             |
| SM1 | 1001 |        | estic bit anomaly / malfunction                                 |             |
|     |      | Bit 0  | Bit ON in case of loss of data kept in                          | R/W         |
|     |      |        | the area "special marker" SM.                                   |             |
|     |      | Bit 1  | Bit ON in case of loss of data kept in                          | R/W         |
|     |      |        | the area "variable V".  |             |
|     |      | Bit 2  | Bit ON in case of loss of data kept in                          | R/W         |
|     |      |        | the area "EEProm".  |             |
|     |      | Bit 3  | Bit ON in case of program load from                             | R/W         |
|     |      |        | flash memory.   |             |
|     |      | Bit 4  | Bit ON in case of reset of the CPU or                           | R/W         |
|     |      |        | intervention of the watch-dog.                                  |             |
|     |      | Bit 5  | Bit ON in case of stack overflow in                             | R/W         |
|     |      |        | the area reserved for RAM.                                      |             |
|     |      | Bit 6  | Bit ON in case of missing calibration                           | R/W         |
|     |      | D'/ 7  | of analog inputs/ outputs                                       | D/M         |
|     |      | Bit 7  | Bit ON in case of anomaly /                                     | R/W         |
|     |      | D:4 0  | malfunction in the EEProm.                                      |             |
|     |      | Bit 8  | Bit ON in case of anomaly /                                     | R/W         |
|     |      | Bit 9  | malfunction in the clock.<br>Bit ON in case of anomaly/         | R/W         |
|     |      | ыгэ    | Bit ON in case of anomaly/<br>malfunction in the analog digital | <b>r/ v</b> |
|     |      |        | converter 16 bits   |             |
|     |      | Bit 10 | Bit ON in case of stack overflow of                             | R/W         |
|     |      |        | the timer interrupt   | 1.7.44      |
|     |      | Bit 11 | Bit ON in case of lost calibration data                         | R/W         |
|     |      |        | for analog input/ output.                                       | 1.7.11      |
|     |      | Bit 12 | Bit ON if analog input Al1 out of                               | R           |
|     |      |        | range.  |             |
|     |      | Bit 13 | Bit ON if analog input Al2 out of                               | R           |
| 1   |      |        |   |             |

| frange.Bit 14Bit ON if analog input Al3 out of                                    |     |  |
|---|-----|--|
|   | R   |  |
| frange.   |     |  |
| Bit 15 Bit ON if analog input AI4 out o   | R   |  |
| frange.   |     |  |
| SM2 1002 Device address   |     |  |
| Address (word) of ModBus protocol of the  | R/W |  |
| device. At startup, if <b>SM1.0 = 1</b> , the value                               |     |  |
| becomes initialized to "1", otherwise the   |     |  |
| previously saved data are maintained.   |     |  |
| SM3 1003 Cycle time   |     |  |
| Time of the last scanning cycle of the  | R   |  |
| program (resolution 100µS).   |     |  |
| SM4 1004 Minimal cycle time   |     |  |
| The minimal time found of the program scan  | R   |  |
| cycle (resolution 100µS).   |     |  |
| SM5 1005 Maximum cycle time   |     |  |
| The maximum time found of the program   | R   |  |
| scan cycle (resolution 100µS).  |     |  |
| SM6 1006 Interval of timer interrupt n°1  |     |  |
| SM7 1007 Interval of timer interrupt n°2  |     |  |
|   | R/W |  |
| interrupt. The value can be set between 1   |     |  |
| and 100 ms (example:: SM6=1 $\rightarrow$ 1 ms,                                   |     |  |
| SM6=100 $\rightarrow$ 100 ms). For values of SM6 and                              |     |  |
| SM7 not between 1 and 100, the  |     |  |
| correspondent interrupt is fixed to a default to                                  |     |  |
| 100 ms. At startup they are both fixed to a default of 100 $\rightarrow$ 100 ms.  |     |  |
|   |     |  |
| In the Ladder code of the two interrupts, it is not allowed to use functions that |     |  |
| access the areas of EEPROM and MMC.   |     |  |
| SM8 1008 LCD contrast   |     |  |
|   | R/W |  |
| At startup, if $SM1.0 = 1$ , the value is   |     |  |
| initialized to $50 \rightarrow 50\%$ , otherwise the                              |     |  |
| previously saved data is maintained.  |     |  |

| SM9  | 1009                       | Minima  | al lamp time   |     |  |  |
|------|----------------------------|---|--|-----|--|--|
|      | •                          |   | ack-lighting display 01000                               | R/W |  |  |
|      |                            | $\rightarrow$ 01000 minutes, 0 $\rightarrow$ always lit. At |  |     |  |  |
|      |                            | startup   | , if $SM1.0 = 1$ , the value is initialized              |     |  |  |
|      |                            |   | always lit, otherwise the previously                     |     |  |  |
|      |                            |   | data is maintained.                                      |     |  |  |
| SM10 | 1010                       | Touch   | screen X   |     |  |  |
| SM11 | 1011                       | Touch   | screen Y   |     |  |  |
|      |                            | Coordir   | nates of the point of contact on the                     | R   |  |  |
|      |                            | LCD dis   | splay (X = 0319, Y = 0239) X=0,                          |     |  |  |
|      |                            | $Y=0 \rightarrow$   | upper left corner When the display                       |     |  |  |
|      |                            | is not b  | eing touched, $X = 500$ , $Y = 500$                      |     |  |  |
| SM12 | 1012                       | Touch   | screen FLAGS   |     |  |  |
|      |                            | Bit 0   | Bit ON in case of event: up, down or                     | R   |  |  |
|      |                            |   | auto-repeat.   |     |  |  |
|      |                            | Bit 1   | Bit ON in case of down touch                             | R   |  |  |
|      | (pressure on the display). |   |  |     |  |  |
|      |                            | Bit 2   | Bit ON in case of up touch (release                      | R   |  |  |
|      |                            |   | of pressure on the display).                             |     |  |  |
|      |                            | Bit 3   | Bit ON in case of touch pressure                         | R   |  |  |
|      |                            |   | (continuous pressure on the display).                    |     |  |  |
|      |                            | Bit 4   | Bit ON in case of touch repeat                           | R   |  |  |
|      |                            |   | (autorepeat event)                                       |     |  |  |
| SM13 | 1013                       | Langua  | age  |     |  |  |
|      |                            | The nu  | mber of languages for the text                           | R/W |  |  |
|      |                            | messag  | ges in the graphics is set from                          |     |  |  |
|      |                            |   | gner. This word defines the language                     |     |  |  |
|      |                            |   | currently visualized text messages (if                   |     |  |  |
|      |                            |   | number of languages set by                               |     |  |  |
|      |                            |   | gner, SM13 can vary from <b>0</b> to <b><i>n-1</i>).</b> |     |  |  |
|      |                            |   | At startup, if <b>SM1.0 = 1</b> , the value is           |     |  |  |
|      |                            |   | nitialized to $0 \rightarrow$ first language, otherwise  |     |  |  |
|      |                            |   | ected language is maintained.                            |     |  |  |
| SM14 | 1014                       |   | er of visualized page                                    | 1   |  |  |
|      |                            |   | nat indicates the number of the                          | R   |  |  |
|      |                            |   | visualized page (default 1, at startup the first         |     |  |  |
|      |                            | page is   | page is always visualized).                              |     |  |  |

| SM15 | 1015 | Number of page to visualize  |     |
|------|------|--|-----|
|      |      | Word that specifies the page number to visualize.<br>Writing the number corresponding to a page physically created from the TdDesigner in this word will cause an immediate jump to that page; otherwise the visualized page will remain as it was before. After the page change, the word is set back to 0 automatically.<br>At startup, if <b>SM1.0 = 1</b> , the value is initialized to $0 \rightarrow$ no change of page, otherwise the page previously chosen is maintained. | R/W |
| SM16 | 1016 | Area of last variable modified   |     |
|      |      | Word that indicates (for a single scan cycle)  | R   |
|      |      | the index corresponding to the last area of  |     |
|      |      | memory saved from the graphics. In detail,   |     |
|      |      | indices correspond to these areas:   |     |

| 1         |  |     |
|-----------|--|-----|
|           | Area word V $\rightarrow 1$  |     |
|           | Area word SM $\rightarrow 2$   |     |
|           | Area word Al $\rightarrow 3$   |     |
|           | Area word TR $\rightarrow 4$   |     |
|           | Area word AQ $\rightarrow 5$   |     |
|           | Area word I  |     |
|           | Area word Q Z 7  |     |
|           | Area word T $\overrightarrow{}$ 8  |     |
|           | Area word PT 7 9   |     |
|           | Area word C 7 10   |     |
|           | Area word PV 7 11  |     |
|           | Area double V 7 12   |     |
|           | Area double SM $\rightarrow$ 13  |     |
|           | Area word M $\rightarrow$ 14   |     |
|           | Area word EEPROM 💪 15  |     |
|           | Area word MMC 🕺 16   |     |
|           | Area byte TX EXP1 🗳 19   |     |
|           | Area byte RX EXP1 $\rightarrow$ 20   |     |
|           | Area byte TX COM2 🗳 21   |     |
|           | Area word AI $\rightarrow$ 3Area word TR $\rightarrow$ 4Area word AQ $\rightarrow$ 5Area word Q $\rightarrow$ 7Area word Q $\rightarrow$ 7Area word T $\rightarrow$ 8Area word PT $\rightarrow$ 9Area word C $\rightarrow$ 10Area word PV $\rightarrow$ 11Area double V $\rightarrow$ 12Area double SM $\rightarrow$ 13Area word EEPROM $\rightarrow$ 15Area word MMC $\rightarrow$ 16Area byte TX EXP1 $\rightarrow$ 20Area byte RX EXP1 $\rightarrow$ 21Area byte RX COM2 $\rightarrow$ 22 |     |
| SM17 1017 | -  | ble |
|           | Word that indicates (for a single scan cycle)  | R   |
|           | the number of the last area of memory saved  |     |
|           | from the graphics. As an example, if the   |     |
|           | graphics modifies the variable <b>VW30</b> , , there   |     |
|           | will be, for the scan cycle following the  |     |
|           | modification, $SM16 = 1$ and $SM17 = 30$ . In  |     |
|           | the successive cycle the two areas will be   |     |
|           | automatically reset to 0.  |     |
| SM18 1018 | Time of buzzer activation (x10ms)  |     |
|           | Time buzzer is active in multiples of 10ms.  | R/W |
|           | The default value is $0xFFFF = 65536 =$  |     |
|           | buzzer extinguished, which is set also at the  |     |
|           | end of the activation. If $SM18 = 0$ , the buzzer  |     |
|           | will extinguish only by touch of the display.  |     |
| SM20 1020 |  |     |
|           | Percentage of time used to execute   | R/W |
|           | r ercentage of time used to execute  |     |

|           | instructions relative to the graphics. Descible                       |     |
|-----------|---|-----|
|           | instructions relative to the graphics. Possible                       |     |
|           | values 10 90 $\rightarrow$ 1090%, default 50 $\rightarrow$            |     |
|           | 50% (half time to graphics and half to PLC).                          |     |
| SM21 1021 |   |     |
|           | Percentage of time used only to execute                               | R/W |
|           | instructions relative to the change of a page.                        |     |
|           | Once executed, the effective management of                            |     |
|           | the time is decided by SM20. Possible                                 |     |
|           | values: $1090 \rightarrow 1090\%$ , default 50                        |     |
|           | →50%  |     |
| SM30 1030 | Seconds   |     |
|           | Internal clock seconds (059)  | R/W |
| SM31 1031 | Minutes   |     |
|           | Internal clock minutes (059)  | R/W |
| SM32 1032 | Hours   |     |
|           | Internal clock hours (023)  | R/W |
| SM33 1033 | Day   |     |
|           | Internal clock day (131)  | R/W |
| SM34 1034 | Month   |     |
|           | Internal clock month (112)  | R/W |
| SM35 1035 | Year  |     |
|           | Internal clock year (099)   | R/W |
| SM36 1036 | Day of the week   |     |
|           | Internal clock day of the week $(0 \rightarrow Sunday)$               | R/W |
|           | 6 → Saturday)   |     |
| SM38 1038 | Digital inputs TTL  |     |
|           | Digital inputs I1I8 of the expansion board                            | R/W |
|           | can be acquired also as TTL threshold; this                           |     |
|           | word indicates the state of these inputs, in                          |     |
|           | particular bit0 $\rightarrow$ state I1 TTL, bit 7 $\rightarrow$ state |     |
|           | I8 TTL.   |     |
| SM40 1040 | Conf. EXP1 in mode Free-port  |     |
| SM41 1041 | Conf. COM2 in mode Free-port  |     |
|           | Word that enables the serial port to function                         | R/W |
|           | in free-port mode and to set its parameters.                          |     |
|           | Enabling this mode, the communications                                |     |
|           | protocol using the serial port will be disabled,                      |     |
| l         |   |     |

|            | 1  |            |            |               |             |     |
|------------|--|------------|------------|---------------|-------------|-----|
| allowing   | allowing direct access to the functions of |            |            |               |             |     |
| transmis   | sion ai                                    | nd recep   | tion of tl | he d          | lata on the |     |
| port. The  |  |            |            |               |             |     |
|            | •  |            |            |               |             |     |
| startup to |  |            |            |               |             |     |
| Bit 0÷3    | These                                      | e bits set | the con    | nmu           | inication   | R/W |
|            |  |            |            |               | n the free- |     |
|            |  | node acc   |            |               |             |     |
|            | •  |            | 0          |               | 0           |     |
|            | follow                                     | ing value  | es (bauc   | :(נ           |             |     |
|            | $0 \rightarrow$                            | 110        | 6          | $\rightarrow$ | 4800        |     |
|            | 1 →  | 150        | 7          | $\rightarrow$ | 9600        |     |
|            | $2 \rightarrow$                            |            | 8          | $\rightarrow$ |             |     |
|            |  |            |            | ÷             |             |     |
|            | · ·  | 600        | 9          |               | 28800       |     |
|            | 4 →  | 1200       | 10         | $\rightarrow$ | 38400       |     |
|            | 5 →  | 2400       | 11         | $\rightarrow$ | 57600       |     |

|              |      | Bit 4÷7<br>Bit 8  | port communication data in the<br>free-port mode: 7-8 = number of<br>data bits, N = No parity control,<br>O = Odd parity, E = Even parity,<br>1,2= number of stop bits.<br>$0 \rightarrow 8, N, 1  6 \rightarrow 8, N, 2$<br>$1 \rightarrow 8, 0, 1  7 \rightarrow 8, 0, 2$<br>$2 \rightarrow 8, E, 1  8 \rightarrow 8, E, 2$<br>$3 \rightarrow 7, N, 1  9 \rightarrow 7, N, 2$<br>$4 \rightarrow 7, 0, 1  10 \rightarrow 7, 0, 2$<br>$5 \rightarrow 7, E, 1  11 \rightarrow 7, E, 2$<br>Bit set to "1" enables the free-port<br>mode. "0" returns the serial line<br>control to the protocol selected | ₹/W<br>₹/W |
|--------------|------|---|--|------------|
| SM43         | 10/3 | Num By  | during the programming phase.  |            |
| SM43<br>SM44 | 1043 |   | yte in COM2 reception buffer   |            |
|              | 1074 | For each serial line, this word contains the<br>number of valid characters present in the<br>reception buffer. It is used in the free-port<br>mode to control the number of characters<br>received. Anything written to this word will<br>set the value to zero, thus emptying the<br>reception buffer. |  |            |

# SM49 1049 EXP1 serial baud rate (default 9600 baud)

| SM53                                       | 1053 | COM   | 2 serial ba        | ud rate (d   | efau                             | It 57600 baud                                       | )   |
|--|------|---|--------------------|--------------|----------------------------------|---|-----|
|  | •    | The value that is set defines the             |                    |              |                                  |   | R/W |
|  |      | communication velocity of the serial line for |                    |              |                                  |   |     |
|  |      | the ModBus protocol, if enabled (baud):       |                    |              |                                  |   |     |
|  |      |   |                    |              |                                  | s are active, it                                    |     |
|  |      | is nec  | essary to s        | set this wor | d in                             | the   |     |
|  |      | initiali                                      | zation cod         | e. In case r | no m                             | odification is                                      |     |
|  |      | made  | or if modif        | ications are | e ma                             | ade in other  |     |
|  |      | parts   | of the prog        | ram, the ba  | aud                              | rate will   |     |
|  |      |   |                    | fault rate s |                                  |   |     |
|  |      | $0 \rightarrow$                               | 110                | 6            | $\rightarrow$                    | 4800  |     |
|  |      | 1 →   | 150                | 7            | $\rightarrow$                    | 9600  |     |
|  |      | 2 →   |                    | 8            | $\rightarrow$                    | 19200   |     |
|  |      | 3 →   | 600                | 9            | $\rightarrow$                    | 28800<br>38400                                      |     |
|  |      | 4 →   | 1200               |              | $\rightarrow$                    | 38400   |     |
|  |      | <b>5</b> →                                    | 2400               | 11           | $\rightarrow$                    | 57600   |     |
| SM50                                       |      |   |                    | mat (defau   |                                  |   |     |
| SM54                                       | 1054 |   |                    |              |                                  | n modifiable)                                       |     |
|  |      | -   |                    | s set define |                                  | -   | R/W |
|  |      |   |                    |              |                                  | the serial line                                     |     |
|  |      |   |                    | protocol, if |                                  |   |     |
|  |      |   |                    |              |                                  | is are active, it                                   |     |
|  |      |   | •                  | set this wor |                                  |   |     |
|  |      |   |                    |              |                                  | odification is                                      |     |
|  |      |   |                    |              |                                  | ade in other  |     |
| parts of the program, the baud rate will   |      |   |                    |              |                                  |   |     |
| remain at the default rate set at startup. |      |   |                    |              |                                  |   |     |
|  |      |   | 8, N, 1            | 6            |                                  | 8, N, 2   |     |
|  |      |   | 8, O, 1            | (            | マム                               | δ, U, 2   |     |
|  |      |   | 8, E, 1            | 8<br>9       |                                  | ō, E, Z<br>7 N 2                                    |     |
|  |      | $\begin{vmatrix} 3 \\ 4 \end{vmatrix}$        | 7, N, 1<br>7, O, 1 | 9            | $\stackrel{\prime}{\rightarrow}$ | i, N, Z   |     |
|  |      | $5 \rightarrow$                               | 7, 0, 1<br>7, E, 1 | 10           | ź                                | 8, O, 2<br>8, E, 2<br>7, N, 2<br>7, O, 2<br>7, E, 2 |     |
|  |      | 5 /   | <i>1</i> , ⊑, I    | 11           | ,                                | 1, E, Z   |     |

| SM51 | 1051 | EXP1 RX/TX delay (default 100 mS) |
|------|------|-----------------------------------|
|      |      |                                   |

| SM55 | 1055 | COM2 RX/TX delay (default 1 mS)                          |     |
|------|------|--|-----|
|      |      | The value set in mS defines:                             | R/W |
|      |      | <ul> <li>Protocol slave: The minimum delay</li> </ul>    |     |
|      |      | between the end of the serial reception of               |     |
|      |      | data coming from the master device to the                |     |
|      |      | start of transmission of the data of the reply           |     |
|      |      | from the TD320 (max 100 mS).                             |     |
|      |      | <ul> <li>Protocol master: The maximum waiting</li> </ul> |     |
|      |      | period between the start of the transmission             |     |
|      |      | of interrogation data by the TD240, to the               |     |
|      |      | completed reception of the reply data from a             |     |
|      |      | slave device.  |     |
| SM52 | 1052 |  |     |
| SM56 | 1056 |  |     |
|      |      | The value set in this word defines the                   | R/W |
|      |      | number of consecutive communication errors               |     |
|      |      | after which an anomaly will be signaled in the           |     |
|      |      | respective bit of the word "Serial state". The           |     |
|      |      | default value for all of the ports is 10.                |     |
| SM73 |      | Serial state EXP1 1-16                                   |     |
| SM74 |      | Serial state EXP1 17-32                                  |     |
| SM75 |      | Serial state EXP1 33-48                                  |     |
| SM76 |      | Serial state EXP1 49-64                                  |     |
| SM77 | -    | Serial state EXP1 65-80                                  |     |
| SM78 |      | Serial state EXP1 81-96                                  |     |
| SM79 |      | Serial state EXP1 97-112                                 |     |
| SM80 | 1080 |  |     |
| SM81 | 1081 | Serial state EXP1 129-144                                |     |
| SM82 | 1082 | Serial state EXP1 145-160                                |     |
| SM83 | 1083 | Serial state EXP1 161-176                                |     |
| SM84 | 1084 | Serial state EXP1 177-192                                |     |
| SM85 | 1085 | Serial state EXP1 193-208                                |     |
| SM86 | 1086 | Serial state EXP1 209-224                                |     |
| SM87 | 1087 | Serial state EXP1 225-240                                |     |
| SM88 | 1088 | Serial state EXP1 241-256                                |     |

|       |      | These words contain the state of EXP1 serial communication. Each bit of each word signals a condition of missing communication (off line) or error for each of the data transmitted or received using the instructions EXP_1-256 (for example, <b>SM80.4=1</b> indicates an error in the instruction number <b>EXP-117()</b> ). In the case of a serial line set to slave protocol, the error condition is signalled by putting a "1" in all of the bits of the word SM73. | R |
|-------|------|--|---|
| SM89  | 1080 | At startup, all of the words are initialized to 0. Serial state COM2 1-16  |   |
| SM90  |      | Serial state COM2 17-32  |   |
| SM91  |      | Serial state COM2 33-48  |   |
| SM92  |      | Serial state COM2 49-64  |   |
| SM93  |      | Serial state COM2 65-80  |   |
| SM94  |      | Serial state COM2 81-96  |   |
| SM95  |      | Serial state COM2 97-112   |   |
| SM96  | 1096 | Serial state COM2 113-128  |   |
| SM97  | 1097 | Serial state COM2 129-144  |   |
| SM98  | 1098 | Serial state COM2 145-160  |   |
| SM99  | 1099 | Serial state COM2 161-176  |   |
| SM100 | 1100 | Serial state COM2 177-192  |   |
| SM101 | 1101 | Serial state COM2 193-208  |   |
| SM102 | 1102 | Serial state COM2 209-224  |   |
| SM103 | 1103 | Serial state COM2 225-240  |   |
| SM104 | 1104 | Serial state COM2 241-256  |   |
|       |      | These words contain the state of COM2  | R |
|       |      | serial communication. From the moment this   |   |
|       |      | port can be set just as ModBus slave, the  |   |
|       |      | error condition is signalled by putting a "1" in   |   |
|       |      | all of the bits of the word SM89. At startup,  |   |
|       |      | all of the words are initialized to 0.   |   |
| SM107 |      | EXP1 time-out number   |   |
| SM109 | 1109 | COM2 time-out number   |   |

|       |      | If the corresponding part is get to a Master                        | R   |
|-------|------|---|-----|
|       |      | If the corresponding port is set to a Master                        | ĸ   |
|       |      | protocol, this indicates the number of non-                         |     |
|       |      | received packets of information during the                          |     |
|       |      | communication.  |     |
|       |      | At startup, all counts are initialized to 0.                        |     |
| SM108 | 1108 |   |     |
| SM110 | 1110 | COM2 number of errors   | _   |
|       |      | If the corresponding port is set to a Master                        | R   |
|       |      | protocol, this indicates the number of packets                      |     |
|       |      | of information with errors during the                               |     |
|       |      | communication.  |     |
|       | •    | At startup, all counts are initialized to 0.                        |     |
| SM112 | 1112 |   |     |
| SM113 | 1113 | COM2 minimum delay for new transmission                             | า   |
|       |      | If the corresponding port is set to a Master                        | R/W |
|       |      | protocol, this sets the minimum delay for a                         |     |
|       |      | new transmission after the reply of a slave                         |     |
|       |      | device.   |     |
|       |      | Possible values $0100 \rightarrow 0100$ ms,                         |     |
|       |      | default 5 → 5ms   |     |
| SM120 | 1120 | State of digital inputs I1+I16 during test sta                      | ge  |
|       |      | This word sets the state of digital inputs                          | R/W |
|       |      | during test stage (SM0.5=1). Each bit of this                       |     |
|       |      | word corresponds to the state of one digital                        |     |
|       |      | input, starting with less significant bit                           |     |
|       |      | $(SM120.0 \rightarrow I1, SM120.15 \rightarrow I16)$ . This word is |     |
|       |      | automatically set to zero at the starting of                        |     |
|       |      | TD240.  |     |
| SM121 | 1121 | Filter on analog inputs I1+I16 (default 10 means                    | )   |
|       |      | It is possible to filter digital inputs signals by                  | R/W |
|       |      | setting a delay time. If the state of input                         |     |
|       |      | changes, the new state will be accepted only                        |     |
|       |      | if the input will maintain it for the                               |     |
|       |      | programmed time. Data will be accepted                              |     |
|       |      | after that the filter will have eliminated noises                   |     |
|       |      | and fixed input lines on stable values.                             |     |
|       |      | TD240 supports filters with delay times                             |     |
|       |      | between 0 and 50 ms.  |     |
| L     |      | 50  |     |

| SM122 1122 | Filter on analog inputs (default 5 means) |  |        |
|------------|---|--|--------|
|            |   | sible to apply a filter to analog inputs   | R/W    |
|            |   | the number of values which are used  |        |
|            | •   | the mean for the final value of input or   |        |
|            |   | ively for each input exclude the   |        |
|            |   | e filter (means) and/or the control  |        |
|            |   | which automatically discards the   |        |
|            |   | sions supposed to be false (very   |        |
|            |   | t from previous value)   |        |
|            | Bit 0÷3                                   | Questi bit impostano il numero di valori   | R/W    |
|            |   | da mediare per il calcolo del valore   | -      |
|            |   | dell'ingresso.   |        |
|            |   | 115 $\rightarrow$ numero conversioni utilizzate                                    |        |
|            |   | per il calcolo della media.  |        |
|            |   | These bits set the number of values  |        |
|            |   | which are used to rate the mean for the  |        |
|            |   | final value of input 115 $\rightarrow$ Number of conversions used to rate the mean |        |
| -          | Bit 4                                     | Exclude software filter for input AI1.   | R/W    |
|            |   | $0 \rightarrow$ Filter enabled $1 \rightarrow$ Filter excluded                     | 1      |
| Bit 5      |   | Exclude software filter for input Al2.   | R/W    |
|            |   | $0 \rightarrow$ Filter enabled $1 \rightarrow$ Filter excluded                     | 1      |
|            | Bit 6                                     | Exclude software filter for input AI3.   | R/W    |
|            |   | $0 \rightarrow$ Filter enabled $1 \rightarrow$ Filter excluded                     | 1      |
|            | Bit 7                                     | Exclude software filter for input AI4.   | R/W    |
|            |   | $0 \rightarrow$ Filter enabled $1 \rightarrow$ Filter excluded                     | 1., ., |
|            | Bit 8                                     | Exclude software filter for input AI5.   | R/W    |
|            |   | $0 \rightarrow$ Filter enabled $1 \rightarrow$ Filter excluded                     | 10,00  |
|            | Bit 9                                     | Exclude software filter for input AI6.   | R/W    |
|            |   | $0 \rightarrow$ Filter enabled $1 \rightarrow$ Filter excluded                     |        |
|            | Bit 10                                    | Discard conversions supposed false AI1.  | R/W    |
|            |   | $0 \rightarrow$ enabled $1 \rightarrow$ desabled                                   |        |
|            | Bit 11                                    | Discard conversions supposed false AI2.  | R/W    |
|            |   | $0 \rightarrow$ enabled $1 \rightarrow$ desabled                                   |        |
|            | Bit 12                                    | Discard conversions supposed false AI3.  | R/W    |
|            | <b>B</b> 14 4 5                           | $0 \rightarrow$ enabled $1 \rightarrow$ desabled                                   | _      |
|            | Bit 13                                    | Discard conversions supposed false Al4.  | R/W    |
|            |   | $0 \rightarrow$ enabled $1 \rightarrow$ desabled                                   |        |

|       |      | Bit 14 | Discard conversions supposed false AI5.<br>$0 \rightarrow$ enabled $1 \rightarrow$ desabled | R/W |  |  |
|-------|------|--------|---|-----|--|--|
|       |      | Bit 15 | Discard conversions supposed false Al6.<br>$0 \rightarrow$ enabled $1 \rightarrow$ desabled | R/W |  |  |
| SM123 | 1123 | Config | uration analog input Al1  |     |  |  |
| SM124 | 1124 | Config | Configuration analog input Al2  |     |  |  |
| SM125 | 1125 | Config | Configuration analog input Al3  |     |  |  |
| SM126 | 1126 | Config | uration analog input AI4  |     |  |  |
| SM127 | 1127 | Config | uration analog input AI5  |     |  |  |
| SM128 | 1128 | Config | uration analog input AI6  |     |  |  |

|         |      | These special markers select the type of         | R/W |
|---------|------|--|-----|
|         |      | sensor connected to analog inputs AI1AI6         |     |
|         |      | (It is also necessary to set properly the        |     |
|         |      | dipswitches). At starting AI1AI4 are             |     |
|         |      | automatically set as linear input 010V-          |     |
|         |      | 10bit, while AI5AI6 are not enabled.             |     |
|         |      | $0 \rightarrow \text{Desabled input}$            |     |
|         |      | $1 \rightarrow$ Linear input 0÷10V (ris. 10 bit) |     |
|         |      | $2 \rightarrow$ Linear input 0÷10V (ris. 16 bit) |     |
|         |      | $3 \rightarrow$ Linear input 0÷1V                |     |
|         |      | $4 \rightarrow$ Linear input 0÷20mV              |     |
|         |      | $5 \rightarrow$ Linear input 0÷20mA              |     |
|         |      | $6 \rightarrow$ Linear input 4÷20mA              |     |
|         |      | 7 $\rightarrow$ Input thermocouple type K        |     |
|         |      | $8 \rightarrow$ Input thermocouple type S        |     |
|         |      | $9 \rightarrow$ Input thermocouple type T        |     |
|         |      | 10 → Input thermocouple type R                   |     |
|         |      | 11 $\rightarrow$ Input thermocouple type J       |     |
|         |      | 12 → Input thermocouple type E                   |     |
|         |      | 13 $\rightarrow$ Not available                   |     |
|         |      | 14 → Input PT100 PT100                           |     |
|         |      | 15 → Input PT100 NI100                           |     |
|         |      | 16 $\rightarrow$ Compensation input PT100/NI100  |     |
|         |      | (only for PT100/NI100 3wires. Option             |     |
|         |      | valid only for AI2 and AI3, respectively         |     |
|         |      | compensation for AI1 and AI4)                    |     |
|         |      | $17 \rightarrow Not available$                   |     |
|         |      | 18 → Input NTC-10K β=3435                        |     |
|         |      | 19 $\rightarrow$ Input for conversion countings  |     |
|         |      | 20 → PT1000                                      |     |
|         |      | 21 → PT500                                       |     |
|         |      | 22 → PTC-1K (KTY 1000 ohm)                       |     |
|         |      | $23 \rightarrow \text{Light sensor Lux Fi}$      |     |
|         |      | $24 \rightarrow \text{Light sensor Lux Rs}$      |     |
|         | 1129 | Minimum value for analog input Al1 linear        |     |
|         | 1130 | Minimum value for analog input Al2 linear        |     |
|         | 1131 | Minimum value for analog input Al3 linear        |     |
| SM132 1 | 1132 | Minimum value for analog input Al4 linear        |     |

| SM157  | 1157                                      | Minimum value analog outputAQ2   |                 |  |  |  |
|--|---|--|-----------------|--|--|--|
| SM156  | 1156                                      | Minimum value analog outputAQ1   |                 |  |  |  |
|  |   | At starting all calibration values are set to 0.   |                 |  |  |  |
| Alx.   |   |  |                 |  |  |  |
| calibration Alx) / 1000 + Offset calibration |   |  |                 |  |  |  |
| Value Alx = Value Alx + (Value Alx * Gain    |   |  |                 |  |  |  |
| The formule is:                              |   |  |                 |  |  |  |
|  |   | error on the reading.  |                 |  |  |  |
|  |   | for Al1Al6. This is useful to correct any  | r <b>x</b> / vv |  |  |  |
| SM152  | 1152                                      | Gain calibration for analog input Al1 Al6<br>These words set the calibration of conversion | R/W             |  |  |  |
| SM151  | 1151                                      | Gain calibration for analog input Al1 Al5  |                 |  |  |  |
| SM150  | 1150                                      | <u> </u>   |                 |  |  |  |
| SM149  | 1149                                      | U  |                 |  |  |  |
| SM148  | 1148                                      | Gain calibration for analog input Al1 Al2  |                 |  |  |  |
| SM147  | 1147                                      | Ŭ  |                 |  |  |  |
| SM146  |   | Offset calibration for analog input Al6  |                 |  |  |  |
| SM145  |   | Offset calibration for analog input AI5  |                 |  |  |  |
| SM144  |   | Offset calibration for analog input Al4  |                 |  |  |  |
| SM143  |   | Offset calibration for analog input Al3  |                 |  |  |  |
| SM142  |   | Offset calibration for analog input Al2  |                 |  |  |  |
| SM141  |   | Offset calibration for analog input Al1  |                 |  |  |  |
| 014444                                       |   | is set to 1000.  |                 |  |  |  |
|  | minimum value is set to 0 and the maximum |  |                 |  |  |  |
|  |   | (Alx,Min,Max)instruction. At starting the  |                 |  |  |  |
|  |   | words are modified directly by RANGE   |                 |  |  |  |
|  |   | configured as linear input (V or mA). These  |                 |  |  |  |
|  |   | limits for analog conversion of AI inputs if   |                 |  |  |  |
|  |   | Set the minimum and maximum numeric  | R/W             |  |  |  |
| SM140  | 1140                                      | Max value for analog input Al6 linear  |                 |  |  |  |
| SM139  | 1139                                      | Max value for analog input AI5 linear  |                 |  |  |  |
| SM138  | 1138                                      | Max value for analog input Al4 linear  |                 |  |  |  |
| SM137  | 1137                                      | Max value for analog input AI3 linear  |                 |  |  |  |
| SM136  | 1136                                      | Max value for analog input AI2 linear  |                 |  |  |  |
| SM135  | 1135                                      | Max value for analog input Al1 linear  |                 |  |  |  |
| SM134  | 1134                                      | Minimum value for analog input Al6 linear  |                 |  |  |  |
| SM133  | 1133                                      | Minimum value for analog input AI5 linear  |                 |  |  |  |

| SM158 | 1158                                   | Minimum value analog outputAQ3           |     |  |  |
|-------|--|--|-----|--|--|
| SM159 | 59 1159 Minimum value analog outputAQ4 |  |     |  |  |
|       |  | The value of analog output AQ            | R/W |  |  |
|       |  | corresponding to voltage output 0,0V.    |     |  |  |
|       |  | These words are modified directly by     |     |  |  |
|       |  | RANGE (AQx,Min,Max)instruction. They are |     |  |  |
|       |  | automatically set to 0 at starting.      |     |  |  |
| SM160 | 1160                                   | Max value analog outputAQ1               |     |  |  |
| SM161 | 1161                                   | Max value analog outputAQ2               |     |  |  |
| SM162 | 1162                                   | Max value analog outputAQ3               |     |  |  |
| SM163 | 1163                                   | Max value analog outputAQ4               |     |  |  |
|       |  | The value of analog output AQ            | R/W |  |  |
|       |  | corresponding to voltage output 10,0V.   |     |  |  |
|       |  | These words are modified directly by     |     |  |  |
|       |  | RANGE (AQx,Min,Max)instruction. They are |     |  |  |
|       |  | automatically set to 100 at starting.    |     |  |  |

| SM164 | 1164 | Frequency of analog/digital converter (default 55 Hz)  |     |
|-------|------|--|-----|
|       |      | Conversion frequency expressed in Hz for<br>the analog/digital converter. This parameter<br>allows to change conversion speed in order<br>to get stable or faster conversions,<br>depending on the applications. Frequency<br>range is between 18 Hz (slower and<br>therefore more accurate conversion) to 1920<br>Hz (faster and therefore less accurate<br>conversion)   | R/W |
| SM165 | 1165 | ,  |     |
| SM166 | 1166 | Conversion reference for inputAI2 (default 0)  |     |
| SM167 | 1167 | Conversion reference for inputAI3 (default 0)  |     |
| SM168 | 1168 | Conversion reference for inputAI4 (default 0)  |     |
|       |      | Reference used by analog-digital converter<br>for the conversion of analog inputs AI. These<br>special markers allow to change the default<br>reference (0 = AI-COM) from analog round to<br>one of the other analog inputs, making a<br>differential reading between two inputs AI.<br>Allowed options:<br>$0 \rightarrow AI-COM$<br>$1 \rightarrow AI1$ $3 \rightarrow AI3$<br>$2 \rightarrow AI2$ $4 \rightarrow AI4$ | R/W |

| SM169   | 1169                           | SETUP    | register of A/D converter (default 10)                         |     |  |  |
|---------|--------------------------------|----------|--|-----|--|--|
|         |                                | This SN  | A allows to change some settings of                            | R/W |  |  |
|         |                                | the inte | rnal A/D converter. This register is                           |     |  |  |
|         |                                |          | ed by bit, but not all bit can be                              |     |  |  |
| modifie |                                |          | d:   |     |  |  |
| Bit 7+5 |                                | Bit 7+5  | Not used, keep value "0"                                       |     |  |  |
|         |                                |          | Divisor of conversion speed                                    |     |  |  |
|         |                                |          | $0 \rightarrow$ standard conversion speed                      |     |  |  |
|         |                                |          | 1 → halved conversion speed                                    |     |  |  |
|         |                                | Bit 3    | Not used, keep value "1"                                       |     |  |  |
|         |                                |          | Reference tension of converter VREF                            |     |  |  |
|         |                                |          | $0 \rightarrow$ internal reference 1,25 V                      |     |  |  |
|         |                                |          | 1 → internal reference 2,50 V                                  |     |  |  |
|         |                                |          | Buffer input of converter                                      |     |  |  |
|         |                                |          | $0 \rightarrow$ buffer desabled                                |     |  |  |
|         | $1 \rightarrow$ buffer enabled |          |  |     |  |  |
|         |                                | Bit 0    |  |     |  |  |
| SM170   | 1170                           |          | register of A/D converter (default 64)                         |     |  |  |
|         |                                |          | A allows to change some settings of                            | R/W |  |  |
|         |                                |          | rnal A/D converter. This register is                           |     |  |  |
|         |                                | 0        | ed by bit, but not all bit can be                              |     |  |  |
|         |                                | modifie  |  |     |  |  |
|         |                                | Bit 7    | Not used, keep value "0"                                       |     |  |  |
|         |                                |          | Conversion format  |     |  |  |
|         |                                |          | $0 \rightarrow bipolar$  |     |  |  |
|         |                                |          | 1 → unipolar   |     |  |  |
|         |                                |          | Internal filter of converter                                   |     |  |  |
|         |                                |          | $00 \rightarrow Auto$  |     |  |  |
|         |                                |          | $01 \rightarrow Fast$  |     |  |  |
|         |                                |          | $10 \rightarrow \text{Sinc2}$                                  |     |  |  |
|         |                                |          | $11 \rightarrow \text{Sinc3}$<br>0 \rightarrow buffer desabled |     |  |  |
| Bit 3+0 |                                | Bit 3+0  |  |     |  |  |
|         |                                |          | 1 →buffer enabled  | -   |  |  |
|         |                                | Bit 0    | Not used, keep value "0"                                       |     |  |  |
| SM171   | 1171                           | GAIN r   | egister of A/D converter (default 0)                           |     |  |  |

|            | Change the gain value for input amplifier        |  |                             |     |  |
|------------|--|--|-----------------------------|-----|--|
|            | (called PGA) of the converter. The value of      |  |                             | R/W |  |
|            | this register is significant only for the analog |  |                             |     |  |
|            | •  | •  | countings (SM123126         |     |  |
|            |  | 0  | managed by bit, but         |     |  |
|            | ,  | oit can be modi                            |                             |     |  |
|            |  | Not used, keep                             |                             | -   |  |
|            |  |  | · input amplifier "PGA"     |     |  |
|            |  | $000 \rightarrow 1$                        | $100 \rightarrow 16$        |     |  |
|            | Bit 2÷0  | $000 \rightarrow 1$<br>$001 \rightarrow 2$ |                             |     |  |
|            | BIt 2÷0  |  | $101 \rightarrow 32$        |     |  |
|            |  | $010 \rightarrow 4$                        | 110 → 64<br>111 → 128       |     |  |
| 01470 4470 | 0000   |  |                             |     |  |
|            | OFFSET register for A/D converter (default 0)    |  |                             |     |  |
|            | Enter an offset value for the input of internal  |  |                             | R/W |  |
|            | A/D converter. The value of this register is     |  |                             |     |  |
|            | significant only for the analog inputs           |  |                             |     |  |
|            | •  |  | gs (SM4043 = 19).           |     |  |
|            |  |  | ged by bit, but not all bit |     |  |
| (          | can be modified:                                 |  |                             |     |  |
|            | Sign of offset value:                            |  |                             |     |  |
|            | $0 \rightarrow$ Positive Offset                  |  |                             |     |  |
|            | 1 → Negative Offset                              |  |                             |     |  |
|            | Input offset value:                              |  |                             |     |  |
|            |  | · · ·                                      | (VREF * Offset value) /     |     |  |
|            |  | (254 * <b>PGA</b> )                        |                             |     |  |

### 8.3 Area of Digital Input I

Memory area I is composed of **32 words** and can be used to contain the state of the digital inputs read through the serial lines of other devices.

It is organized in words: each of the 16 bits of a word can represent the state of an input. It is accessible also in bits, in order to allow the control of each single input.

### 8.4 Area of Digital Output Q

Memory area Q is composed of **32 words** and can be used to contain the state of the digital outputs to then write them on serial lines of other devices.

It is organized in words: each of the 16 bits of a word can represent the state of an output. It is accessible also in bits, in order to allow the control of each single output.

### 8.5 Area of Marker M

Memory area M is comprised of **50 words** and contains the state of all the markers (contact bits) used in the program. It is organized in words: each of the 16 bits of a word represents the state of a marker. For example, the state of the marker M5 is memorized in the bit 4 of word 1 in memory area M. The marker M5 is thus accessible as M1.4 (contact bit of the word), but also as single bit M5 (contact or electrical relay coil).

### 8.6 Area of Analog Inputs Al

Memory area AI is composed of **32 words** and can be used to contain the state of the analog inputs read from the serial lines of other devices.

It is organized in words: each can represent the state of an analog Input.

### 8.7 Area of Analog Outputs AQ

Memory area AI is composed of **32 words** and can be used to contain the state of the analog outputs read from the serial line of other devices. It is organized in words: each can represent the state of an analog output.

#### 8.8 Areas of Timer T and Preset Timer PT

The area of memory for timer T is composed of **128 words**. If the timer is enabled, the variation of the contents of the area of memory is regulated by the type of timer, which is set at the moment of activation.

The area of memory for preset timer PT is composed of **128 words** and contains the values of activation of the contacts (preset) of the respective timers. The areas are organized in signed words, thus the resolution of the timer and the preset timer is 16 bits (+32767).

### 8.9 Area of Counters C and Preset Counters PV

The memory area for counters C is composed of **64 words**. If a counter is enabled, the variation of the contents of the memory area is regulated by the type of counter.

The memory area for preset counters PV is composed of **64 words** and contains the values of activation of the (preset) contacts of the respective counters. The areas are organized in words, thus the resolution of the counters and preset counters is 16 bits (from -32768 to +32767).

### 8.10 Area of Bistable Relay B

The area of memory for bistable relay B is composed of **128 bits**. It is organized by bits, thus each bistable relay is individualized by a single bit.

### 8.11 Area of EEProm

The area of memory EEProm is composed of **1000 words**. This memory is storage for data that must be maintained even if the TD320 remains off for very long periods (over 6 months). The data saved in this area are in fact tested at startup to verify their integrity, and any anomalies are signalled by activating the bit **SM1.2**, causing the initialization of the entire area to 0.

Access and writing to this area require a time significantly longer than any other (order of 30/40 mS), thus it is advisable not to use it for continual access (there is also a limit to the number of times that an EEProm cell can be written to, of an order of 1000000 times), but only to copy at startup the data stored here, for example to memory area V, and then use area V for an access that is more rapid (order of  $5/10\mu$ s).

### 8.12 Area of MMC

The memory area MMC is composed of **3000 words**. This is the memory storage where it is possible to save large quantities of data and maintain it even in the absence of power. The memory is of type EEProm. The resulting access is thus slower than area V and SM and the TD240 executes no control of the integrity of the data stored in this area.

#### 8.13 Area of TX/RX EXP1

The memory area TX/RX EXP1 is composed of **200 bytes**. This area is used to manage the data in transit on the serial port EXP1. The first 100 bytes (TX-0...TX-99) are used to load the data to transmit, the last 100 bytes (RX-0...RX-99) are used to save the data received by the serial port EXP1.

These bytes are useful only in the free-port mode, while in normal mode they are managed directly by the protocol selected in the programming phase.

## 9 Communication protocols

The TD240 can communicate with all devices that support the following serial protocols:

- ModBus RTU
- Nais Matsushita master

The terminal has 2 serial ports of communication (EXP, COM2), analyzed from the electrical point of view in chapter 3. The ports are each managed in a different manner and will be analyzed separately.

### 9.1 Managing the communication port

The communication between the TD240 and other devices is managed by the PLC part of the terminal, thus the configuration of the port and the instructions must be implemented in the development environment PLProg 4.xx. Generally the coils of the Ladder diagram are executed following the sequential order written in the diagram itself. The instruction related to the coil at line "n+1" is not executed until the full completion of the instruction related to the coil at line "n" (for coils positioned in the same column).

The control of transmission and reception of data is instead **asynchronous** with respect to the cycle of execution of the Ladder code.

When an instruction of read/write of a device must be executed (line "n"), control passes immediately to the next instruction (line "n+1"), without waiting for the data to be effectively read/written.

The effective transfer of the data in the serial line is executed in a manner that is independent to the normal scan of Ladder code, in different times according to the port that is used.

### 9.1.1 Port EXP1

The port EXP1 can be configured with protocol ModBus (master or slave), or Nais Matsushita master, Control Technique. These are the ports typically used for communication with other devices (PLC, etc.).

The control of the communication is carried out every 1 mS. This means that the corresponding flow of serial data will be controlled 1000 times per second.

### 9.1.2 Port COM2

The port COM2 can be configured only by using protocol ModBus slave. This port is used for programming the terminal by PC.

The control of the communication is carried out every scan cycle of the Ladder code.

This means that the flow of the data in the serial port COM2 will be controlled one time at the end of each scan cycle.

### 9.2 Protocol ModBus RTU

**ModBus on the serial line is a Master-Slave protocol**. In a network with this type, there is a single node (the Master) that interrogates and commands the Slaves and processes the results. The Slave nodes typically do not transmit data unless specifically requested by the Master and do not communicate directly between each other.

A device in the serial line (a network node) is uniquely determined by an identification number (ID, variable from 1 to 255), called the ModBus Slave address: two devices cannot have the same address.

The addressees of a request (one or more Slave nodes) are selected by the Master by their ID, thus the data that transits on the line has a precise destination.

The Master controls the line: it doesn't have a specific ID address and can read or write data in words or bits with one or more Slave devices, specifying the destination ID. Data read or written is saved in the destination device in registers identified by a specific ModBus address (variable from 1 to 65535). Each ModBus address can correspond to a register (word area of memory) or a single bit of a register (particular bit of an area of memory).

Refer to the following figure for the list of possible operations in a ModBus communication: reading and writing of a word or bit, single or multiple.

| Caratteristiche prot | Caratteristiche protocollo Modbus RTU           |             |  |  |  |
|----------------------|---|-------------|--|--|--|
| Baud-rate            | Programmabile                                   |             |  |  |  |
| Formato              | 8,N,1 (8 bit, no parità, 1 stop) (default)      |             |  |  |  |
| Funzioni supportate  | BITS READING (0x                                | 01, 0x02)   |  |  |  |
|                      | WORDS READING (max 20 word) (0x0                | 03, 0x04)   |  |  |  |
|                      | SINGLE BIT WRITING                              | (0x05)      |  |  |  |
|                      | SINGLE WORD WRITING                             | (0x06)      |  |  |  |
|                      | MULTIPLE BITS WRITING                           | (0x0F)      |  |  |  |
|                      | MULTIPLE WORDS WRITING (max 20 word)            | (0x10)      |  |  |  |
| Codici di errore     | ILLEGAL FUNCTION CODE                           | (0x01)      |  |  |  |
|                      | ILLEGAL DATA ADDRESS                            | (0x02)      |  |  |  |
|                      | ILLEGAL DATA VALUE                              | (0x04)      |  |  |  |
| Broadcast            | Scrittura simultanea a tutti gli slave collega  | ati usando  |  |  |  |
|                      | l'indirizzo 0x00 e senza nessuna risposta da j  | oarte degli |  |  |  |
|                      | slave.  |             |  |  |  |
| Interrogazione con   | Interrogazione con indirizzo 0xFF a cui rispond | e qualsiasi |  |  |  |
| indirizzo slave      | slave collegato.                                |             |  |  |  |
| sconosciuto          |   |             |  |  |  |

### 9.2.1 ModBus RTU Master

The protocol ModBus Master can be configured only for the port EXP1.

With this configuration the TD240 will have control of the transit of the data of the corresponding port. For each of the two ports, there can be active up to 256 frames (active packets) at the same time. Each frame corresponds to an instruction of direct communication:

- **Reading from a Slave:** Reading from the slave at the ModBus address corresponding to the data of interest is memorized in the registers of the Master. Each instruction can read up to 16 consecutive words.
- Writing on a Slave: Data of interest by the Master is written in the slave at the ModBus address corresponding to the data to overwrite. Each instruction can write up to 16 consecutive words.
- **Read/write on a Slave:** Normally data read from the slave is saved in the Master. When the data internal to the TD320 varies by effect of the program, it is useful to write the modified data into the Slave. Each instruction of read/write can operate only on 1 word.

### 9.2.2 ModBus RTU Slave

The protocol ModBus Slave can be configured for all three of the ports EXP1, and COM2.

With this configuration all of the resources of the terminal are available to the Master device that is eventually connected.

The following table indicates all of the data (word and bit) accessible by use of the ModBus protocol. Each area of memory corresponds to a distinct ModBus address (for the access of a word or a bit), variable from 0 to 65536.

The read/write access and the value given at startup of the TD240 are shown for each. Depending upon the initialization values, the following cases occur:

- 1. "**ROM**" fixed values defined by the program.
- 2. "**EEP**" value stored in EEProm memory, maintained for at least 10 years even in the absence of power.
- 3. **"TAMP**" value stored in RAM with the battery buffer. Also this data is maintained in the absence of power, but for a limited time (around 4 to 6 months).

4. "**DEFINED VALUE**" the value given to the data at startup corresponds to the value indicated in the table.

| ACCESS TO WORD |                                   |       |       |  |  |
|----------------|-----------------------------------|-------|-------|--|--|
| MODBUS         | DESCRIPTION                       | READ/ | RESET |  |  |
| ADDRESS        |                                   | WRITE | VALUE |  |  |
| 0              | Type of device                    | R     | ROM   |  |  |
| 1              | Version of Firmware               | R     | ROM   |  |  |
| 2              | Protocol activated on COM1        | R     | ROM   |  |  |
| 3              | Protocol activated on EXP1        | R     | ROM   |  |  |
| 4              | Protocol activated on COM2        | R     | ROM   |  |  |
| 5              | Address of protocol               | R     | TAMP  |  |  |
| 6              | Version of BOOT                   | R     | ROM   |  |  |
| 10             | Clock seconds TD240               | R/W   | TAMP  |  |  |
| 1000 ÷ 1199    | Word area special marker SM       | R/W   | TAMP  |  |  |
| 2000 ÷ 2999    | Word area variable V              | R/W   | TAMP  |  |  |
| 12000 ÷ 12127  | Word area timer T                 | R/W   | 0     |  |  |
| 13000 ÷ 13127  | Word area preset timer PT         | R/W   | 0     |  |  |
| 14000 ÷ 14063  | Word area counters C              | R/W   | 0     |  |  |
| 15000 ÷ 15063  | Word area preset counters PV      | R/W   | 0     |  |  |
| 17000 ÷ 17099  | Word area buffer TX EXP1          | R     | 0     |  |  |
| 17500 ÷ 17599  | Word area buffer RX EXP1          | R     | 0     |  |  |
| 18000 ÷ 18099  | Word area buffer TX COM2          | R     | 0     |  |  |
| 18500 ÷ 18599  | Word area buffer RX COM2          | R     | 0     |  |  |
| 19000 ÷ 19031  | Word area analog input Al         | R     | 0     |  |  |
| 19200 ÷ 19215  | Word area trimmer TR              | R     | 0     |  |  |
| 19400 ÷ 19431  | Word area analog output AQ        | R     | 0     |  |  |
| 19800 ÷ 19927  | Word percentage proportional      |       |       |  |  |
|                | / integral / derived / output PID |       |       |  |  |
| 19800          | % Action proportional PID1        | R     | 0     |  |  |
| 19801          | % Action integral PID1            | R     | TAMP  |  |  |
| 19802          | % Action derived PID1             | R     | TAMP  |  |  |
| 19803          | % Output PID1                     | R     | TAMP  |  |  |
| 19804          | % Action proportional PID2        | R     | 0     |  |  |
| <br>19927      | <br>% Output PID128               | R     | TAMP  |  |  |
| 20000 ÷ 20999  | Word area EEProm                  | R/W   | EEP   |  |  |
| 30000 ÷ 59999  | Word area MMC                     | R/W   | EEP   |  |  |

| ACCESS TO WORD    |  |                |                |  |  |  |
|-------------------|--|----------------|----------------|--|--|--|
|                   |  |                |                |  |  |  |
| MODBUS<br>ADDRESS | DESCRIPTION                                    | READ/<br>WRITE | RESET<br>VALUE |  |  |  |
| 90                | Contacts n.o. positioners<br>POS1÷POS16        | R              | 0              |  |  |  |
| 95                | Contacts n.o. tuning positioners<br>POS1÷POS16 | R              | 0              |  |  |  |
| 100               | Contacts n.o. digital inputs I1 ÷ I16          | R              | 0              |  |  |  |
| 101               | Contacts n.o. digital inputs I17 ÷ I32         | R              | 0              |  |  |  |
| 131               | Contacts n.o. digital inputs I497 ÷ I512       | R              | 0              |  |  |  |
| 150               | Contacts n.o. digital outputs Q1 ÷ Q16         | R              | 0              |  |  |  |
| 151               | Contacts n.o. digital outputs Q17 ÷ Q32        | R              | 0              |  |  |  |
| 181               | Contacts n.o. digital outputs Q497 ÷<br>Q512   | R              | 0              |  |  |  |
| 200               | Contacts n.o. bistable relays B1 ÷ B16         | R/W            | 0              |  |  |  |
| 201               | Contacts n.o. bistable relays B17 ÷ B32        | R/W            | 0              |  |  |  |
| 207               | Contacts n.o. bistable relays B113 ÷<br>B128   | R/W            | 0              |  |  |  |
| 250               | Contacts n.o. marker M1 ÷ M16                  | R              | 0              |  |  |  |
| 251               | Contacts n.o. marker M17 ÷ M32                 | R              | 0              |  |  |  |
| 299               | Contacts n.o. marker M785 ÷ M800               | R              | 0              |  |  |  |
| 300               | Contacts n.o. timer T1 ÷ T16                   | R              | 0              |  |  |  |
| 301               | Contacts n.o. timer T17 ÷ T32                  | R              | 0              |  |  |  |
| 307               | Contacts n.o. timer T113 ÷ T128                | R              | 0              |  |  |  |
| 350               | Contacts n.o. counters C1 ÷ C16                | R              | 0              |  |  |  |
| 351               | Contacts n.o. counters C17 ÷ C32               | R              | 0              |  |  |  |
| 352               | Contacts n.o. counters C33 ÷ C48               | R              | 0              |  |  |  |
| 353               | Contacts n.o. counters C49 ÷ C64               | R              | 0              |  |  |  |

| ACCESS TO BIT     |                                    |                |                |  |  |
|-------------------|------------------------------------|----------------|----------------|--|--|
| MODBUS<br>ADDRESS | DESCRIPTION                        | READ/<br>WRITE | RESET<br>VALUE |  |  |
| 1440              | Contact n.o. positioner POS1       | R              | 0              |  |  |
| 1441              | Contact n.o. positioner POS2       | R              | 0              |  |  |
| 1455              | Contact n.o. positioner POS15      | R              | 0              |  |  |
| 1520              | Contact n.o. tuning position POS1  | R              | 0              |  |  |
| 1521              | Contact n.o. tuning position. POS2 | R              | 0              |  |  |
| 1535              | Contact n.o. tuning position POS15 | R              | 0              |  |  |
| 1600              | Contact n.o. digital input I1      | R/W            | 0              |  |  |
| 1601              | Contact n.o. digital input I2      | R/W            | 0              |  |  |
| 2111              | Contact n.o. digital input I512    | R/W            | 0              |  |  |
| 2400              | Contact n.o. digital output Q1     | R/W            | 0              |  |  |
| 2401              | Contact n.o. digital output Q2     | R/W            | 0              |  |  |
| 2911              | Contact n.o. digital output Q512   | R/W            | 0              |  |  |
| 3200              | Contact n.o. bistable relay B1     | R/W            | 0              |  |  |
| 3201              | Contact n.o. bistable relay B2     | R/W            | 0              |  |  |
| 3327              | Contact n.o. bistable relay B128   | R/W            | 0              |  |  |
| 4000              | Contact n.o. marker M1             | R/W            | 0              |  |  |
| 4001              | Contact n.o. marker M2             | R/W            | 0              |  |  |
| 4799              | Contact n.o. marker M800           | R/W            | 0              |  |  |
| 4800              | Contact n.o. timer T1              | R              | 0              |  |  |
| 4801              | Contact n.o. timer T2              | R              | 0              |  |  |
| 4927              | Contact n.o. timer T128            | R              | 0              |  |  |
| 5600              | Contact n.o. counter C1            | R              | 0              |  |  |
| 5601              | Contact n.o. counter C2            | R              | 0              |  |  |
| <br>5663          | Contact n.o. counter C64           | R              | 0              |  |  |

| 16000 | Bit 0 area special marker SM0    | R/W | TAMP |
|-------|----------------------------------|-----|------|
| 16001 | Bit 1 area special marker SM0    |     | TAMP |
|       |                                  |     |      |
| 19199 | Bit 15 area special marker SM199 | R/W | TAMP |
| 32000 | Bit 0 area variables V0          | R/W | TAMP |
| 32001 | Bit 1 area variables V0          | R/W | TAMP |
|       |                                  |     |      |
| 63999 | Bit 15 area variables V2000      | R/W | TAMP |

### 9.3 Protocol NAIS Matsushita Master

This is the protocol that permits the reading and writing of data (bit of word) of the PLC Nais Matsushita.

Generally, the communications interface is RS232, the velocity is 9600 baud (bits/sec), the format of communications 8,O,1 (8 bits of data, odd parity, 1 stop bit). The following table indicates all of the elements that can be read/written from the PLC. The address of the bit o of the word to read or write is obtained by adding the real address of the bit/word (between Min and Max) to the value indicated in the column Offset. Each instruction "EXP" can read or write to several consecutive data locations, the maximum number for each type of data is indicated in the column "Max number bit/word read/written consecutively".

| ACCESS TO BIT      |     |     |      |        |                |  |
|--------------------|-----|-----|------|--------|----------------|--|
| CONTACT            | SYM | MIN | MAX  | OFFSET | READ/<br>WRITE | MAX NUMBER<br>OF BITS READ /<br>WRITTEN<br>CONSECUTIVELY |
| EXTERNAL<br>INPUT  | Х   | 0   | 9999 | 0      | R              | 8  |
| EXTERNAL<br>OUTPUT | Y   | 0   | 9999 | 10000  | R/W            | 8  |
| INTERNAL<br>RELAY  | R   | 0   | 9999 | 20000  | R/W            | 8  |
| LINK<br>RELAY      | L   | 0   | 9999 | 30000  | R/W            | 8  |
| TIMER              | Т   | 0   | 9999 | 40000  | R              | 8  |
| COUNTER            | С   | 0   | 9999 | 50000  | R              | 8  |

| ACCESS TO WORD           |     |     |      |        |                |   |
|--------------------------|-----|-----|------|--------|----------------|---|
| WORD NAME                | SYM | MIN | MAX  | OFFSET | READ/<br>WRITE | MAX NUMBER<br>OF WORDS READ<br>/ WRITTEN<br>CONSECUTIVELY |
| EXTERNAL<br>INPUT        | Х   | 0   | 999  | 0      | R              | 10  |
| EXTERNAL<br>OUTPUT       | Y   | 0   | 999  | 1000   | R/W            | 10(R) / 7 (W)   |
| INTERNAL<br>RELAY        | R   | 0   | 999  | 2000   | R/W            | 10(R) / 7 (W)   |
| LINK<br>RELAY            | L   | 0   | 999  | 3000   | R/W            | 10(R) / 7 (W)   |
| TIMER                    | Т   | 0   | 999  | 4000   | R              | 10  |
| COUNTER                  | С   | 0   | 999  | 5000   | R              | 10  |
| INDEX<br>REG. X          |     | 0   | 0    | 6000   | R/W            | 1   |
| INDEX<br>REG. Y          |     | 0   | 0    | 6001   | R/W            | 1   |
| INDEX<br>REG. D          |     | 0   | 0    | 6002   | R/W            | 1   |
| DATA<br>REGISTER         | DT  | 0   | 9999 | 10000  | R/W            | 10(R) / 7 (W)   |
| LINK DATA<br>REGISTER    | LD  | 0   | 9999 | 20000  | R/W            | 10(R) / 7 (W)   |
| FILE<br>REGISTER         | FL  | 0   | 9999 | 30000  | R/W            | 10(R) / 7 (W)   |
| SET VALUE<br>AREA        |     | 0   | 9999 | 40000  | R/W            | 10(R) / 7 (W)   |
| ELAPSED<br>VALUE<br>AREA |     | 0   | 9999 | 50000  | R/W            | 10(R) / 7 (W)   |

For the two examples shown below, the protocol NAIS Matsushita is selected for the port EXP1.

The illustrated instructions that follow write the contents of the 8 words from V10 to V17 of the TD240 in the register EXTERNAL

### OUTPUT of the PLC NAIS from Y3 to YA (Y10).

| Numero bobina                  |                |                      |
|--------------------------------|----------------|----------------------|
| EXP_ 1                         | •              |                      |
| - Parametri                    |                |                      |
| Azione el indirizzo slave      |                |                      |
| Srivi sullo SLAVE numero       | ▼ 1            | Min 0<br>Max 255     |
| Indirizzo Word\Bit             |                | N:- 0                |
| Bit numero                     | ▼ 10003        | - Min 0<br>Max 65535 |
| Area (Dest. per lettura, Sor.) | per scrittura) |                      |
| Area memoria V word            | ▼ 10 ▼         |                      |
| Numero Word\Bit letti\scritti  | consecutivi    | Min. 0               |
| N* word                        | • 8            | - Min 0<br>Max 16    |

The following illustration, however, reads the register DATA REGISTER of the PLC NAIS, the 10 words from DT0 to DT9, and copies them in the area of V0 to V9 of the TD240.

| Numero bobina   |                     |                    |
|---|---------------------|--------------------|
| EXP_ 1  |                     |                    |
| Parametri   |                     |                    |
| Azione e indirizzo slave<br>Leggi dallo SLAVE numero      | • 1                 | Min 0<br>Max 255   |
| Indirizzo Word\Bit<br>Word numero                         | - 10000             | Min 0<br>Max 65535 |
| Area (Dest. per lettura Sor. per s<br>Area memoria V word | scrittura)<br>• 0 • |                    |
| Numero Word\Bit letti\scritti cor<br>N° word              | rsecutivi<br>10     | Min 0<br>Max 16    |

# 10 Ladder programming of TD240

Programming the PLC part of the TD240 is accomplished with the development environment **PLProg 4.xx**, which provides the user with all the resources necessary for creation of the Ladder diagram. The compilation and download procedure discussed in chapter 4 allows the TD240 terminal to achieve the desired functionality. The following describes all available elements (contacts and coils) and the relative characteristics for the creation of the diagram.

### 10.1 Digital input contacts I

Digital input contacts I can contain the state of the inputs read via serial lines of other devices, up to a maximum of 512.

A contact normally open (N.O.) is closed (ON) when the bit value is "1" (input active). A contact normally closed (N.C.) is opened (ON) when the bit value is "0" (input non-active).

### 10.2 Digital output contacts Q

The TD240 has 512 type "Q" outputs. These can be used to contain the state of eventual outputs of other devices, communicated by serial lines. Each output has a coil and a related logical contact N.O. (normally open) or N.C. (normally closed). At activation of the coil "Q", the related logical contact will close (if normally open) or will open (if normally closed).

#### 10.3 Bistable relay B

There are 128 bistable relays available in the TD240. Each has a coil and related logical contact normally open or closed (N.O/N.C). At activation of coil "B", the related logical contact will change state, if it was closed it will open, if it was open it will close. A contact N.O. is closed (ON) when the bit value is "1". A contact N.C. is opened (ON) when the bit value is "0". At the startup of the terminal, a contact N.O. will be open.
#### 10.4 Timer T

The TD240 has 128 timers of 16 bits. Each is available in three modes of functioning:

- **TON** "on-delay" of activation: time begins counting when the coil is activated (ON). The timer bit (contact T) will be activated when the current timer value (word T) becomes greater than or equal to the pre-established time (preset, word PT). When the coil is deactivated (OFF), the current value of the timer is reset (zeroed). The timer stops in any case when it reaches the maximum value in signed 16-bits (+32767).
- TOFF "off-delay" of deactivation: allows delaying the deactivation of an output for a given period of time after the input has been deactivated. When the coil is activated (ON), the time bit (contact T) is immediately activated and the current value of the timer (word T) will be set to "0". At the deactivation of the coil, the timer will count until the elapsed time becomes greater than or equal to the pre-established time (preset, word PT). Once reached, the timer bit deactivates and the current value stops advancing. If the input remains inactive for a time that is less than the pre-established time, the timer bit remains active. To start the count, the TOFF operation should sense a transition from state active to non-active (ON → OFF).
- TONR with memory: time begins counting when the coil is active (ON). The timer bit (contact T) is active when the current timer value (word T) becomes greater than or equal to the preestablished time (preset, word PT). When the coil is deactivated (OFF), the current value of the timer is maintained. Thus it is possible to accumulate time for more periods of activation of the coil. The current value of the timer can be reset with the operation MOV(Tx = #0). The timer stops in any case when it reaches the maximum value in signed 16-bits (+32767)..è attiva (ON).

The time base can be selected between 10 mS, 100 mS, and 1S for each mode of functioning.

The current value of the timer is a multiple of the selected time base. For example, a current value of 50 in a timer with a base time of 10 mS corresponds to 500 mS, and with a base time of 1 S corresponds to 50 S.

The preset timer (PT) value can be a constant, or the contents of an area VW, SMW, AI, or TR.

#### 10.5 Counters C

TD240 has 64 counters of 16 bits. These are available in two modes of functioning:

- **MUP forward counter:** the counter bit (contact C) is activated when the current value (word C) is greater than or equal to the pre-established value (PV). The counts increments each time the input of the up-count Cx(UP) is active and decrements each time the input of the down-count Cx(DOWN) is active. The counter will be set to zero upon activation of the reset input Cx(RESET) or when the operation MOV(Cx=#0) is executed. Upon reaching the maximum value (32767), the rise of the next up-count will leave the current value unchanged. Similarly, upon reaching the minimum value (-32768) the rise of the next down-count will leave the current value unchanged. For the forward counters, the pre-established value (PV) is compared with the current value at the end of each cycle of the program. If the value is greater than or equal to the preset value, the counter bit activates (counter C), otherwise it is deactivated.
- **MDOWN backward counter**the counter bit (contact C) is activated when the current value (word C) becomes equal to zero. The counter decrements from a pre-established value (PV) on the rise of the input of down-count Cx(DOWN) and increments on the rise of the input of up-count Cx(UP). Upon reaching the maximum value (32767), the rise of the next upcount will leave the current value unchanged. The counter

resets the count bit (contact C) and loads the preset value (PV) when the input Cx(RESET) becomes active. The counter in backward mode will stop counting when it reaches zero.

The preset value (PV) can be a constant, or the contents of an area VW, SMW, AI, or TR.

#### **10.6 Mathematic formulas (FM)**

The functions of math formulas FM execute mathematical operations (+, -, \*, /, | [OR: logical inclusive or], & [logical AND], ^ [XOR: logical exclusive or], << [ROL: ROtate shift Left], >> [ROR: ROtate shift Right) between two operators and saves the result in another memory location. The operators can be numeric (constants) or refer to the available areas of memory (variables).

#### **10.7 MOV assignments**

The function MOV (move) assigns a numeric value (constant) or the contents of another location (source area) to a specified location in memory (destination area). An instruction such as MOV(A=B) copies the contents of the memory location B to the memory location A.

#### **10.8 BLKMOV** multiple assignments

The function BLKMOV (block move) assigns a numerical value or the value from another (source) block of memory to a destination block of memory.

An instruction such as BLKMOV(Ai=Bi, num. data 8) copies the contents of memory Bi into the location of memory Ai, the contents of location Bi+1 into the location Ai+1, .....and the contents of Bi+7into the location Ai+7.

#### **10.9 MOVIND indexed assignments**

MOVIND (move with index offset) assigns a numerical value (constant) or the value from another location of memory (variable source) to the specified location of memory (destination) as offset by an index for the source and/or destination.

This type of assignment permits various memory areas to be used as vectors of N locations each, where the value taken from another location is used as an "index". It is possible to access the values n=0, n=1, ..., n=N-1 of the area.

An instruction such as MOVIND(A[B]=C[D]) copies the contents of the memory location C[D] into the location A[B]. The index of area C is specified by D, which can be another memory location, and similarly B is the index of area A.

#### **10.10 MOVTXT assignments**

MOVTXT saves string characters passed as a function parameter to a specified location in memory. This function permits the following types of characters of the string in the memory area:

- ONE\_CHARACTER\_PER\_WORD: in this format, each word of the destination area will contain a single character of the source string.
- TWO\_CHARACTERS\_PER\_WORD in this format, each word in the destination area will contain two characters of the source string, starting with the high part. If string = "Example" then V[0] = Ex, V[1] = am, V[2] = pl. V[3] = e.

#### 10.11 Digital input immediate contacts II

The digital input contacts II allow the immediate reading of the digital input state. The contact normally open is closed (ON) when the bit value is "1" (input active). The contact normally closed is open (ON) when the bit value is "0" (input non-active).

#### 10.12 Contacts IF

The operations of conditional IF compare the values of two variables of any area of memory. It is possible to carry out the following types of comparison: = (equal), >= (greater than or equal), <= (less than or equal), > (more than), < (less than), <> (not equal). The contact is active when the comparison is true.

#### **10.13 Functions SBIT and RBIT**

The function SBIT (set bit) puts a "1" in a bit of a memory area when the coil of the function is at the active state.

The function RBIT (reset bit) puts a "0" in a bit of a memory area when the coil of the function is at the active state.

The index of the bit varies from 0 to 15 (the destination area is always a word), where bit 0 is the least-significant bit (LSB).

#### 10.14 BIT contacts

This operation extracts the value of a bit of an area of memory. A contact normally open is closed (ON) when the bit value is "1". A contact normally closed is open (ON) when the bit value is "0". The index of the bit varies from 0 to 15 (the destination area is always a word), where bit 0 is the least-significant bit (LSB).

#### **10.15 RANGE functions**

The function RANGE defines the value of the minimum and maxim limits for the analog inputs AI, for the trimmer TR, for the analog outputs AQ, and for the outputs of the PID.

#### RANGE( AI1, Min 10, Max 200)

The function imposes a minimum limit of 10 and maximum limit of 200 for the analog input AI1. If the analog input AI1 corresponds to a potentiometer (from a PLC via a serial communication), is used to establish the preset (PT) of a timer of base time 100 mS, this

provides a variable time from 1.0 to 20.0 seconds, according to the value of the potentiometer.

If input values exceed the limits set in the RANGE function, the output will be blocked to the minimum or maximum allowed value. As for the output PID, the minimum and maximum values serve to calculate the value of the output generated by the algorithm of regulation. Let us consider the following example:

#### RANGE( PID1, Min 100, Max 500 )

The function imposes the minimum limit of 100 and the maximum limit of 500 for the PID1 output. This means that an output of 0% corresponds to the minimum value imposed (100) and 100% will correspond to an output equal to the maximum value (500).

#### 10.16 NOT contacts

The contact NOT modifies the state of the flow of current. The flow of current stops if it reaches a NOT contact and supplies energy if it doesn't reach it.

The operation NOT inverts the logical value  $(0 \rightarrow 1 \text{ and } 1 \rightarrow 0)$ .

#### 10.17 P and N contacts

The transition positive P contact activates the flow of current for one scan cycle of each transition from OFF to ON. The transition negative N contact activates the flow of current for one scan cycle of each transition from ON to OFF.

The instructions that follow in the diagram are thus executed only once (per scan cycle) for each transition that activates the contact.

#### **10.18 SEND functions**

The function SEND transmits the data through the serial line in free-port mode.

In this mode, enabled by the special markers SM39, SM40, and SM41, the protocol that normally manages the serial port is disabled and the Ladder program takes control of the port and of the transmission and reception buffers.

After having loaded the buffer with the data to transmit, activating the SEND function, which has parameters for the serial port and the number of characters to transmit, will cause the data to be sent on the serial line.

During the transmission phase, the bits SM0.6, SM0.7 or SM0.8 relative to the transmission port are set to "1", while at the end of the transmission they will be set to "0". It is possible to control an eventual reply of a connected device through the control of SM42, SM43, and SM44, which contain the number of characters received and saved in the reception buffer of each serial port. Any writing on any of these special markers causes the emptying of the buffer data in reception of the corresponding port.

Calls to the SEND function before the end of the preceding transmission or with free-port mode disabled are ignored by the program.

#### 10.19 TunePOS and POS functions

The function "TunePOS" executes an auto-tuning procedure, indispensable for extracting the data of reaction time and axis inertia for which a positioning procedure is requested.

The function "POS" executes the positioning ON/OFF of the axis.

The functions operate on the variable area VD (double word), the address of the beginning of the area is requested as a parameter of the functions "TunePOS" and "POS". The following table indicates how the data are organized in the area of the two functions from the address of the specified location:

| Address<br>area VD | Contents   |
|--------------------|--|
| +0                 | Encoder countings                                      |
| +2                 | Countings of positioning setpoint value                |
| +4                 | Countings max absolute positioning gap                 |
| +6                 | Time taken to reach max speed (in tenths of seconds)   |
| +8                 | Status of positioning output (0= stop, 1= forward, -1= |
|                    | backward)  |
| +10                | Countingsof forward inertia                            |

| +12 | Countings of backward inertia              |
|-----|--|
| +14 | Minimum pulse duration (0.2 ms resolution) |
| +16 | Moving countings after a 100 mS pulse      |
| +18 | Moving countings after a 500 mS pulse      |
| +20 | Moving countings after a 1000 mS pulse     |

For correct functioning, it is necessary to proceed as follows:

• Transfer the count of the encoder connected to a remote device (read via a serial line) in the field "Counts for encoder" (beginning area of memory).

- Set the count values to the desired position of the axis in the field "Counts for setpoint positioning".
- Set the count values for the maximum gap of positioning in the field "Counts for absolute maximum gap of positioning".
- Set the time, in decimals of seconds", needed for the axis to attain maximum velocity.
- Activate the function "TunePOS" and wait that the contact TunePOS (normally open) closes to indicate the end of the procedure of auto-tuning the axis. At this point, the inertia data and the reaction time of the axis are automatically memorized in the indicated area of memory, remaining available for the function "POS".
- Deactivate the function "TunePOS".
- Activate the function "POS". When the axis is positioned to the setting imposed (within the pre-established gap), the contact
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POS (normally open) will close, to indicate the end of positioning.

• Activate the outputs FORWARD and BACKWARD, reading the value of the field "Output" (VD+8). If the value of "Output" is "1", it is necessary to activate the output FORWARD, if it is "-1", it is necessary to activate the output BACKWARD, if it is "0" it is not necessary to activate any output.

• Set the value of the field "Output" to zero when the consent of the function "TunePOS" or "POS" is removed, to avoid that the output remains forced to forward or backward.

# The following example shows the segment of Ladder code that implements the axis positions as explained in the procedure:



#### 10.20 EXP functions

The communication function EXP allow programming of the serial port EXP1 for the reading/writing of the data of the connected Slave devices, using the Master protocol selected in the project. Such functions are active only when a protocol of communication of type Master is selected for the serial port within the project, that 81

is, a protocol that allows the TD240 to take control of the line governing the flow of data with the slave devices.

The two functions are analogous, the only change is the serial port

that is referenced. Taking into consideration that an Interface RS485 allows the connection of several devices to the same line,

while the Interface RS232 allows connection of a single device to the TD240.

The instructions are active until the corresponding coil is activated, but keep in mind that, according to the protocol of communication, the time of updating the data can vary significantly and that at the moment of activation of the coil, the data read are not available instantly, but only after a certain time due to the delay of communication.

The instructions EXP use the following parameters:

• Index (it is possible to set a maximum of 256 different serial interrogations on each port).

- Type of operation performed:
- Reading: the TD240 continuously reads the data of the Slave device(s) and memorizes them in an area of internal memory.
- Writing: the TD240 continuously writes the data to an area of internal memory in the Slave device(s).
- Reading/Writing: the TD240 normally reads data of the Slave device and memorizes them in an area of internal memory; at the moment in which such internal data to the TD320 is modified by the program, the variations will be passed automatically to the Slave device through a write instruction (one datum at a time).
- Number of the Slave (address of communication of the Slave device).
- The type of data (word or bit).
- The address ModBus relative to the datum (or data) to transfer.
- The area of internal memory of the TD240 for reading or writing the data.

The number of words (the instructions of reading and writing can transfer up to consecutive 16 bits/words).

#### 10.21 StartPID, PID and SetOutPID functions

The functions StartPID, PID and SetOutPID allow the regulation of a size through an algorithm of action that is proportional, integral, and derived.

The function **StartPID** activates the regulation. The function can be activated a single time at startup or repeated at a later moment permitting the modification "on the fly" of the parameters of regulation. The integral action of the PID is zeroed only by calling the functions and fixing the integral time to "0". Otherwise, even in case of shutdown, the system will initialize the regulation maintaining as point of departure the same percentage of integral action, thus limiting the time of transition.

Parameters of the function **StartPID**:

- Proportional band
- Integral time
- Derived time
- Dead band

The parameters can be inserted in numerical format, or can refer to areas of memory. The integral time is expressed in the units of time in which the function PID is called (for instance, function PID called every 1 second, integral time expressed in seconds). The derived time, however, is expressed with an additional decimal digit with respect to the integral time. The proportional band and the dead band are instead expressed in numeric values equal to the setpoint and the process to regulate.

The parameters of the function **PID**:

- Setpoint
- Process
- Output value
- Type of regulation action

The PID function, after acquiring setpoint, process, type of action and type of output, will set in the variable "Ouput value" the value obtained in the algorithm of regulation. Such a value will be obtained rescaling the percentage of the value between 0 and 83 10000 (0.00%  $\div$  100.00%) between the minimum and maximum value of the PID output set by the RANGE function.

The following table indicates 8 types of regulation and the modulation intervals (the effective value between the interval is determined also by the actions integral and derivative, the table shows only the proportional components):

| Type of regulation action | Intervals of modulation  |
|---------------------------|--|
| Single direct action, 0   | Uscita al 100%<br>Uscita al 0%<br>Setpoint<br>Setpoint - B.P. / 2<br>Setpoint + B.P. / 2 |
| Single direct action, 1   | Uscita al 100%<br>Uscita al 0%<br>Setpoint<br>Setpoint - B.P. / 2<br>Setpoint + B.P. / 2 |
| Single inverse action, 0  | Uscita al 100%<br>Uscita al 0%<br>Setpoint<br>Setpoint + B.P. / 2                        |
| Single inverse action, 1  | Uscita al 100%<br>Uscita al 0%<br>Setpoint<br>Setpoint - B.P. / 2<br>Setpoint + B.P. / 2 |
| Double direct action, 0   | Uscita al 100%<br>Uscita al 0%<br>Setpoint + B.P. / 2<br>Setpoint + B.P.                 |
|                           |  |



The PID function, for correct operation, must be called at the most regular intervals possible, thus by timer, or for more brief and precise times, by an internal interrupt.

The function **SetOutPID** is used for the regulation anticipated by the double function automatic/manual. It serves to avoid oscillation of size control in switching from manual mode to automatic by the PID algorithm.

The function uses the following parameters:

Output value

The Output value is set by the PID automatically calculating the single percentages of the proportional and integral actions. In this mode, at the switching of manual function to automatic, the output value of the PID will take on the value set by manual and will initiate the regulation.

The function thus should be called only during the manual regulation phase, in order to maintain alignment of the output of the PID with that of manual. The function will automatically zero the derived action. The use of this function with the process outside of the proportional band sets the integral action to zero.

#### **10.22 GENSET functions**

The function GENSET automatically generates a setpoint variable rising or falling, with the possibility to set a ramp of acceleration or deceleration. The function GENSET operates on a series of variables in contiguous double words, starting from the location indicated as a parameter to the function.

The following table indicates how the data are organized in the memory area used by the function starting from the address of the specified location:

| Address<br>area VD | Contents   |
|--------------------|--|
| +0                 | State of the GENSET function                           |
|                    | $0 \rightarrow$ Stop or end of movement                |
|                    | $1 \rightarrow$ Initialization function                |
|                    | $2 \rightarrow \text{Ramp of acceleration}$            |
|                    | $3 \rightarrow$ Movement at constant velocity          |
|                    | $4 \rightarrow$ Ramp of deceleration                   |
| +2                 | Initial setpoint / setpoint calculated by the function |
|                    | GENSET (counts)  |
| +4                 | Final setpoint (counts)                                |
| +6                 | Velocity of movement (counts*1000 / time unit)         |
| +8                 | Duration of acceleration ramp (time unit)              |
| +10                | Duration of deceleration ramp (time unit)              |
| +12                | Instantaneous velocity of setpoint (counts*1000 / time |
|                    | unit)  |

For correct functioning, it is necessary to proceed as follows:

- Set the starting setpoint in location VD+2.
- Set the final setpoint in location VD+4.
- Set the maximum velocity of movement in location VD+6 in counts\*1000 / time unit (so as to have 3 decimal digits. For

example, setting 12345 corresponds to a velocity of 12.345 counts / time unit).

• Set the duration of the acceleration ramp in location VD+8 (expressed in time units, if the duration of the phase of acceleration should be 1 second, and the GENSET function is called by an interrupt of 1 mS, set 1000).

• Set the duration of the ramp of deceleration in location VD+10.

• Write "1" in the location VD (the location indicated as parameter of the function). This gives the "start" to the function that will automatically begin to write the generated setpoint in the location VD+2. The location VD will be also updated with the actual state, while the instantaneous velocity of the setpoint expressed with three decimal digits will be written in the location VD+12.

At the end of movement, when the location VD+2 attains the value of the final setpoint, the functional will automatically enter into a standby phase, indicated by the value "0" in the location VD. In this mode, the GENSET function can remain always enabled, even when movement is not necessary.

#### 10.23 CONV functions

The function CONV converts the source data into one of the available formats:

- TO\_7SEG\_SIGNED: Converts the input data (a word with sign -32768..+32767) into a number specified in digits already transformed in code for 7-segment display. The function will take as parameters the number of digits to convert, starting from the least significant digit. The coded data will be saved (one digit per word) starting from the destination word and then in the successive words according to the number of digits requested.
- **TO\_7SEG\_UNSIGNED:** This is analogous to the above con la description with the difference that the data of origin is segno interpreted as a word without sign (0..65535). The code is comprised of a bit set to "1" if a segment should be lit, and if the segment should remain dark. The association between the bits and the segments of the display is the following:



- **TO\_ASCII\_SIGNED:** Convert the input data (a word with sign –32768..+32767) into ASCII-coded digits. The function will take as parameters the number of digits to save. The coded data will be saved (one digit per word) starting from the destination word and then in the successive words according to the number of digits requested.ASCII.
- **TO\_ASCII\_UNSIGNED:** This is analogous to the above description with the difference that the data of origin is interpreted as a word without sign (0..65535).

## 11 Notes / Updates

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